

Fig.1

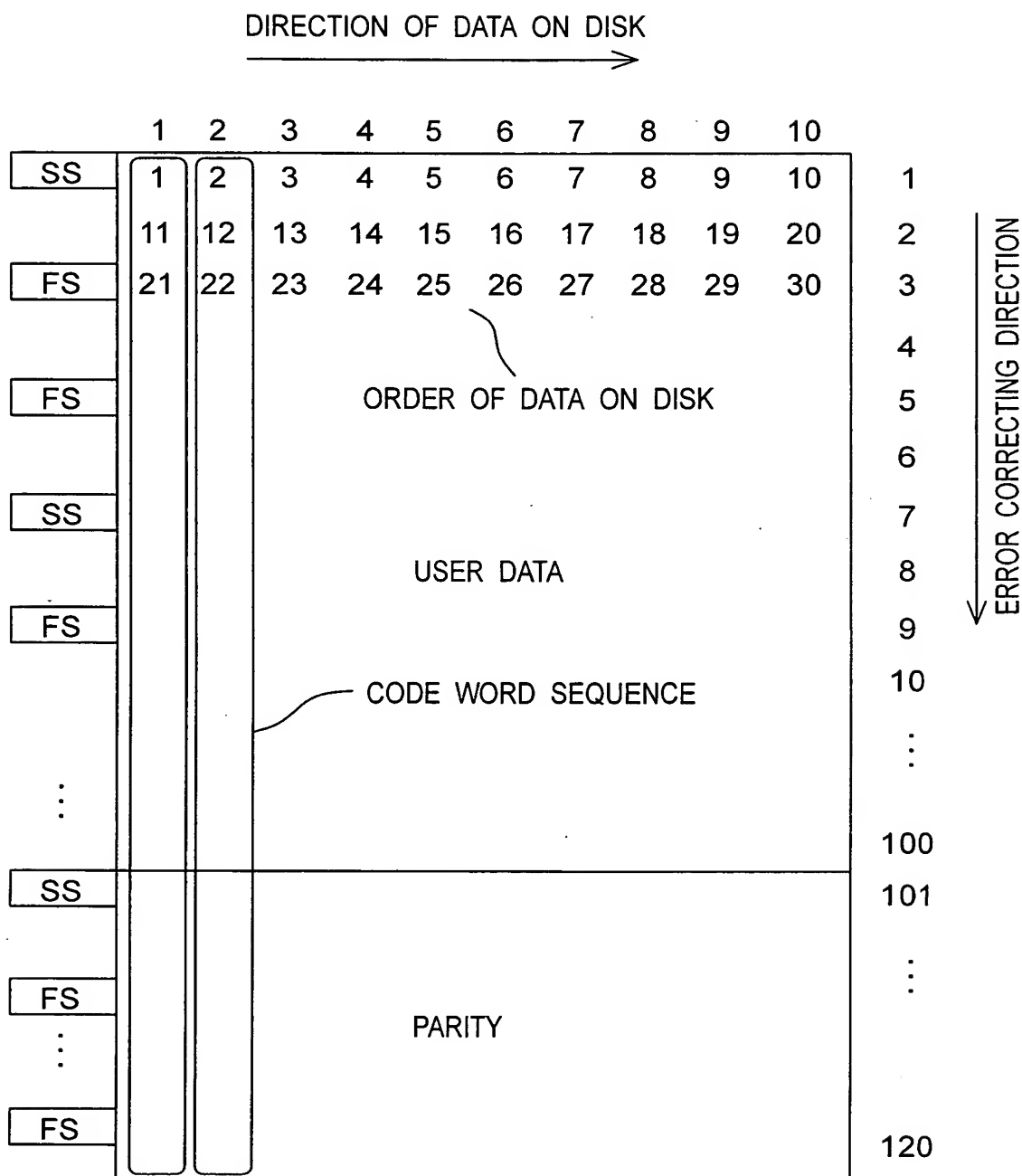


Fig.2

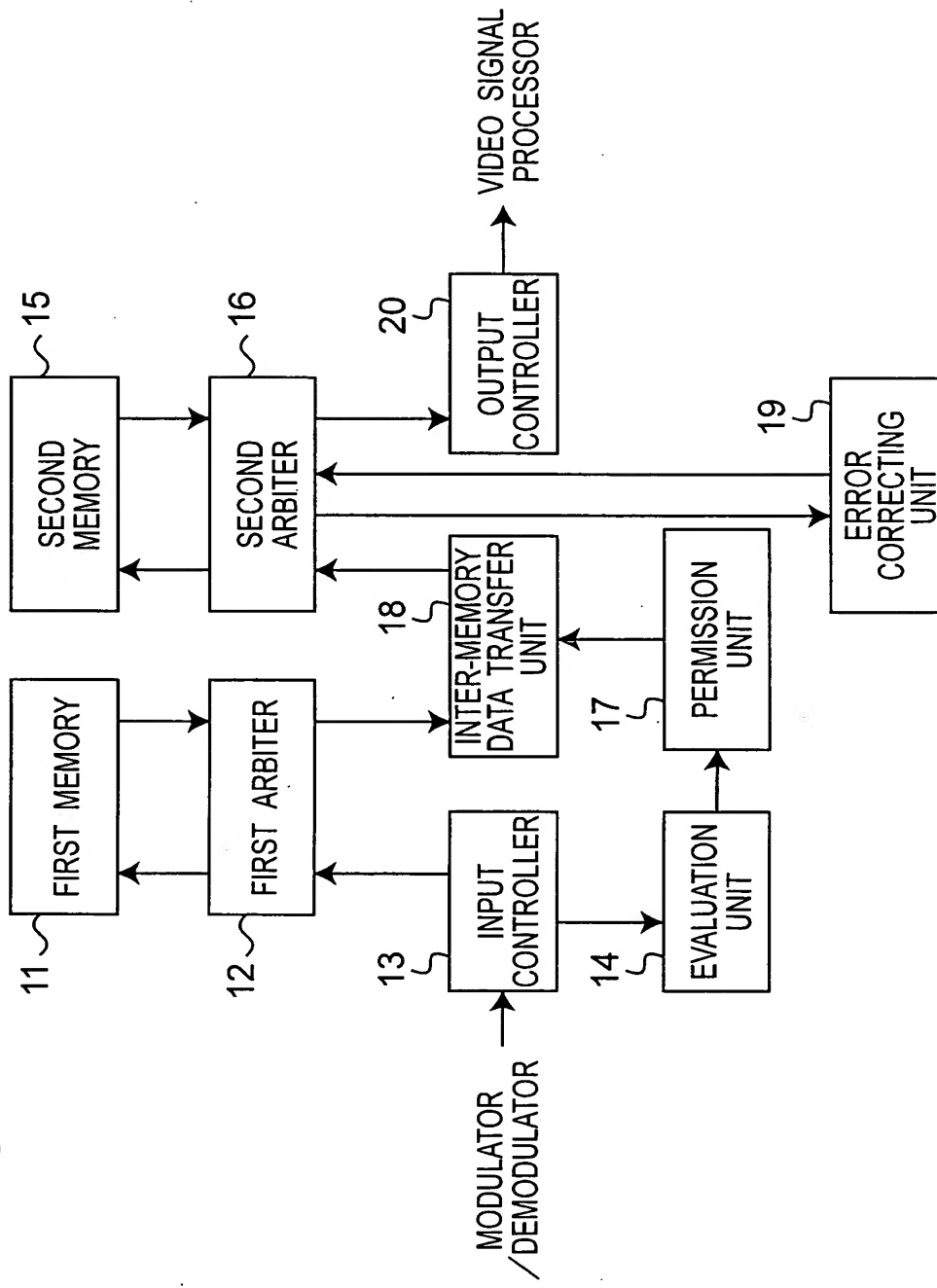


Fig.3

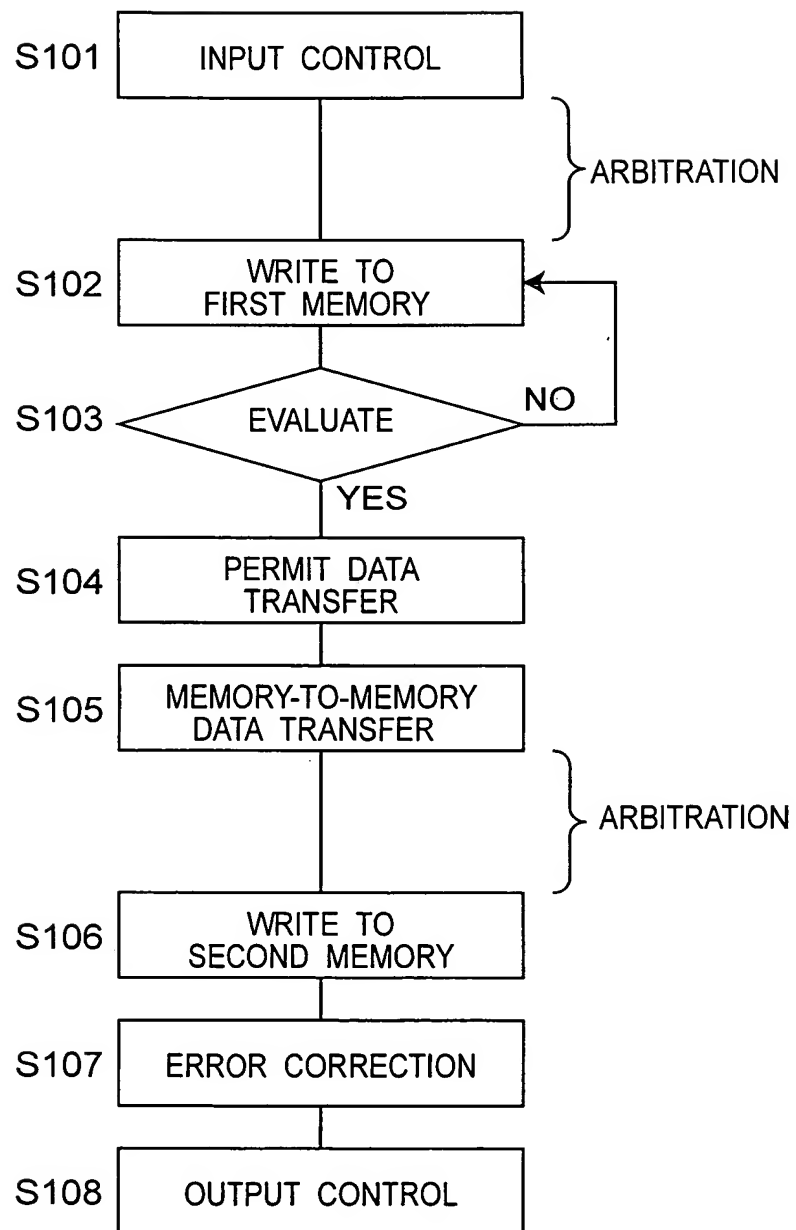


Fig.4

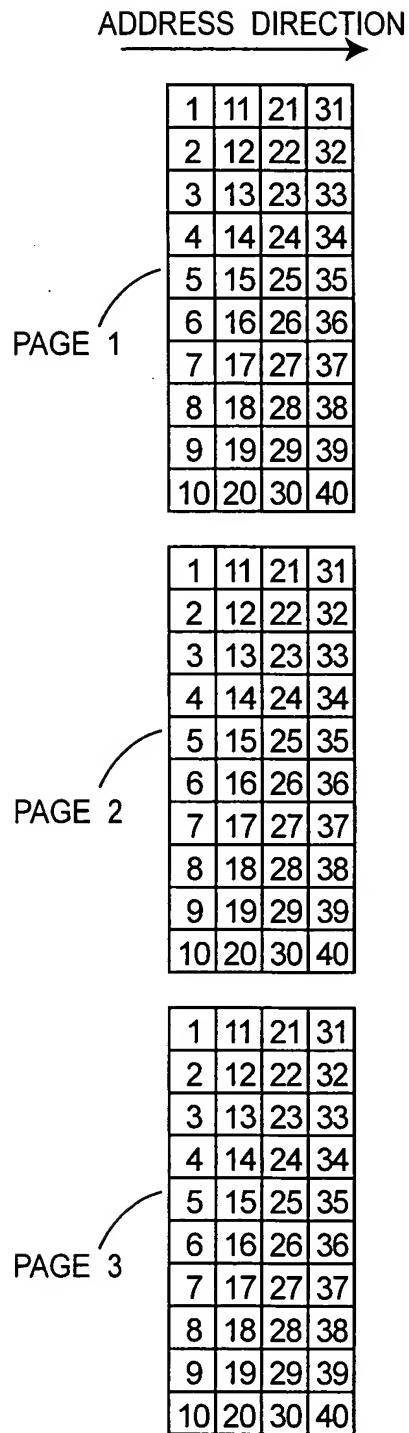


Fig.5

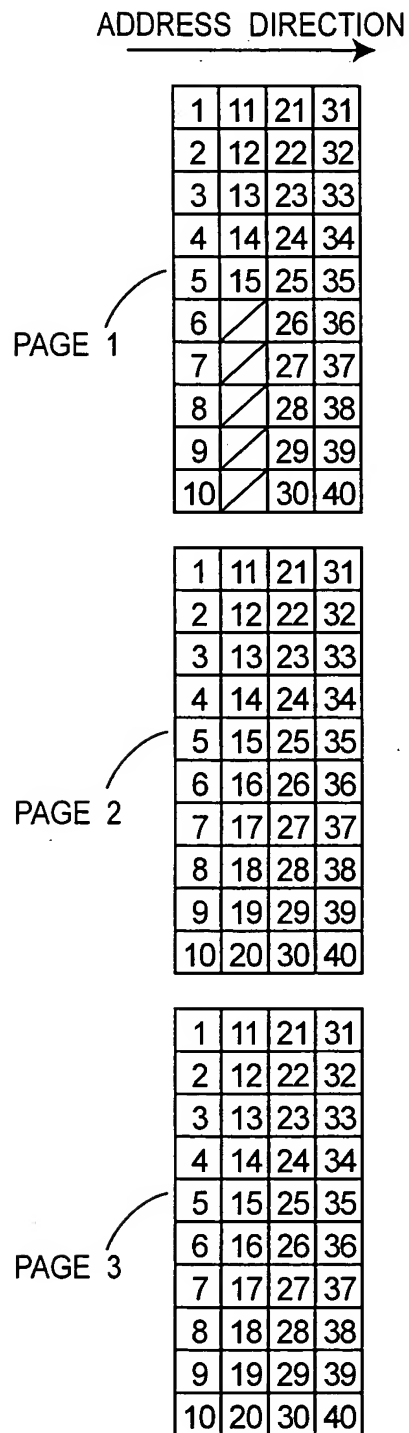


Fig.6

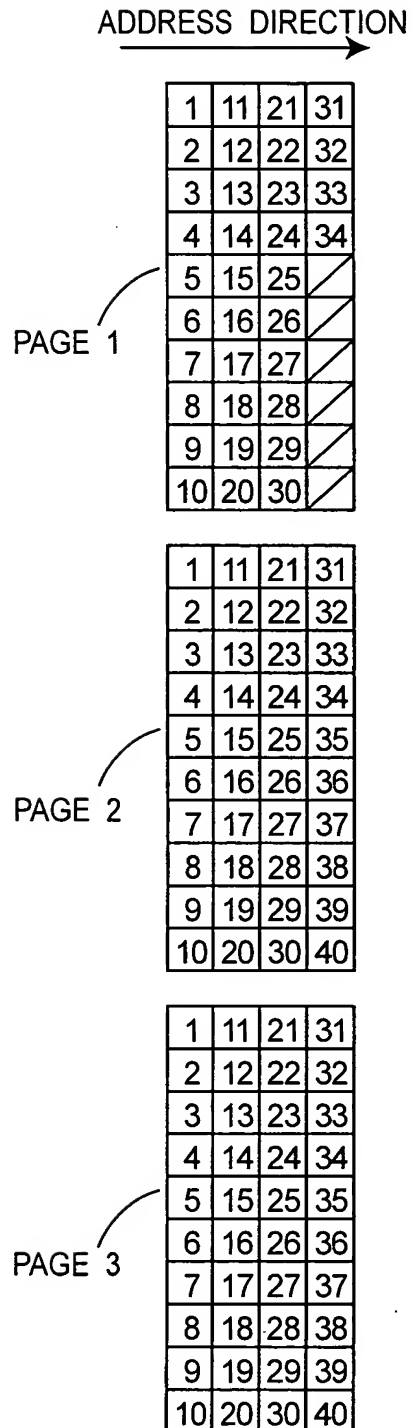


Fig. 7

ADDRESS DIRECTION
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PAGE 1

1	11	/	/
2	12	/	/
3	13	/	/
4	14	/	/
5	15	/	/
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3	13	23	33
4	14	24	34
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6	16	26	36
7	17	27	37
8	18	28	38
9	19	29	39
10	20	30	40

Fig. 8

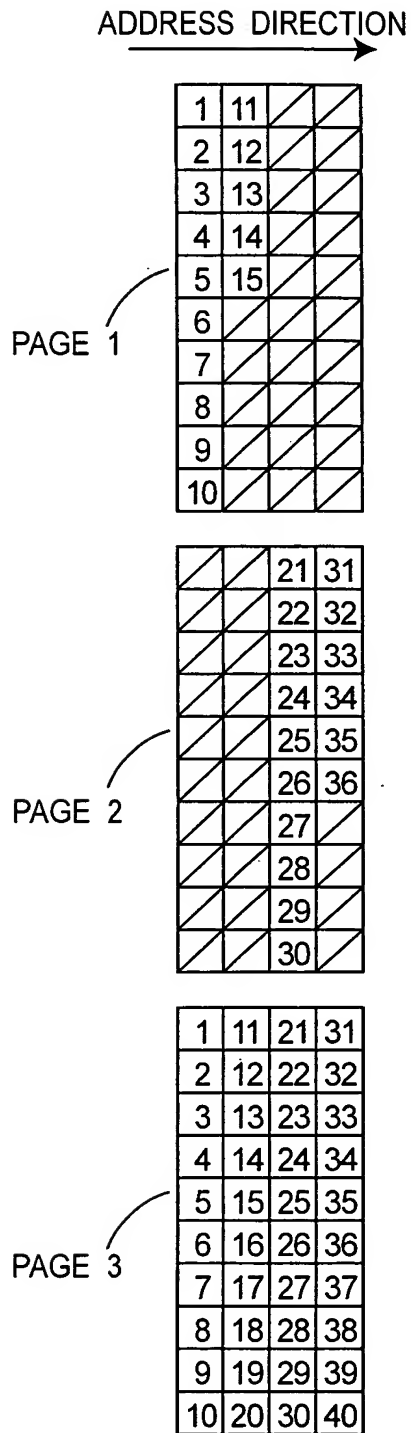


Fig.9

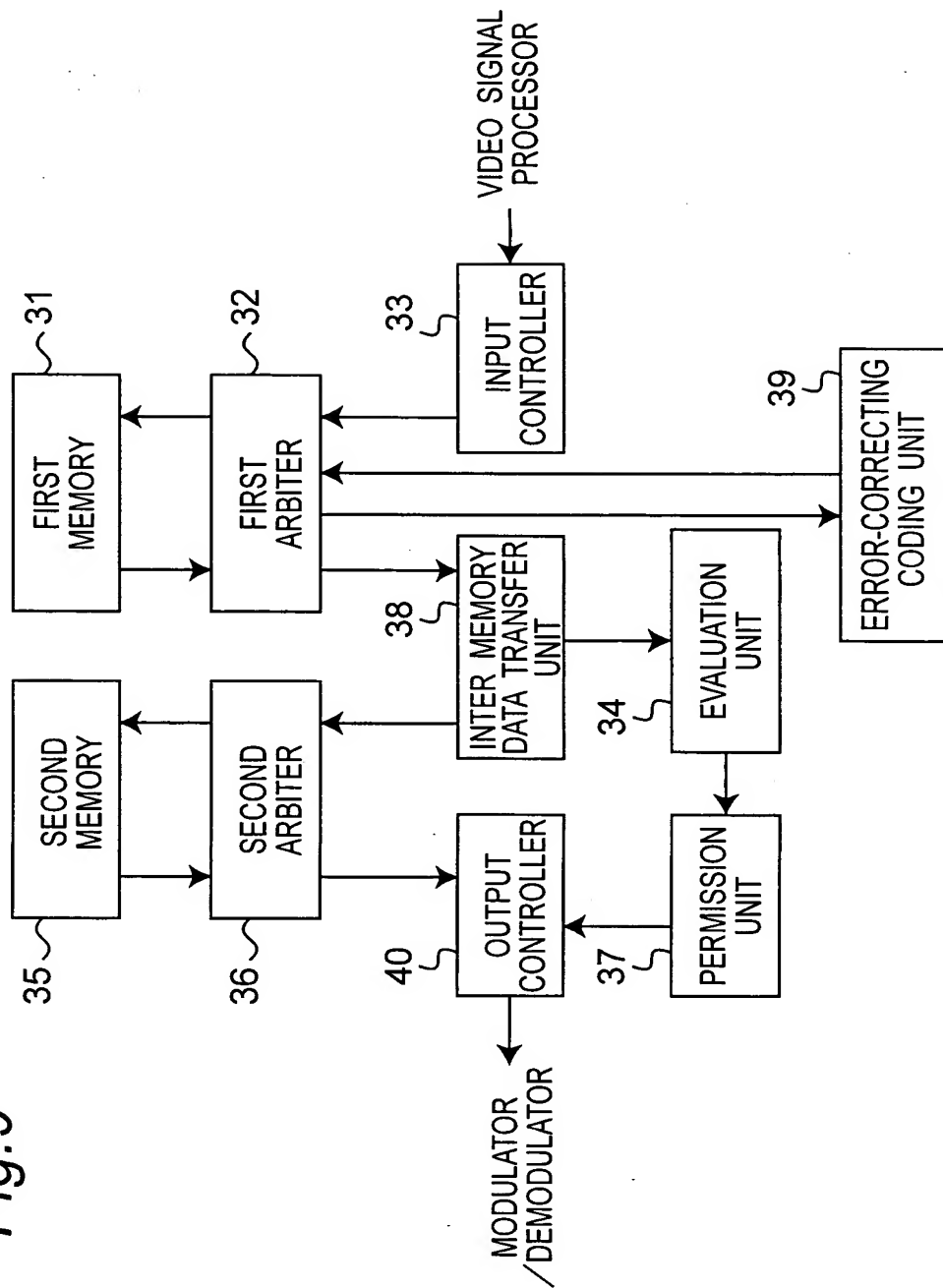


Fig. 10

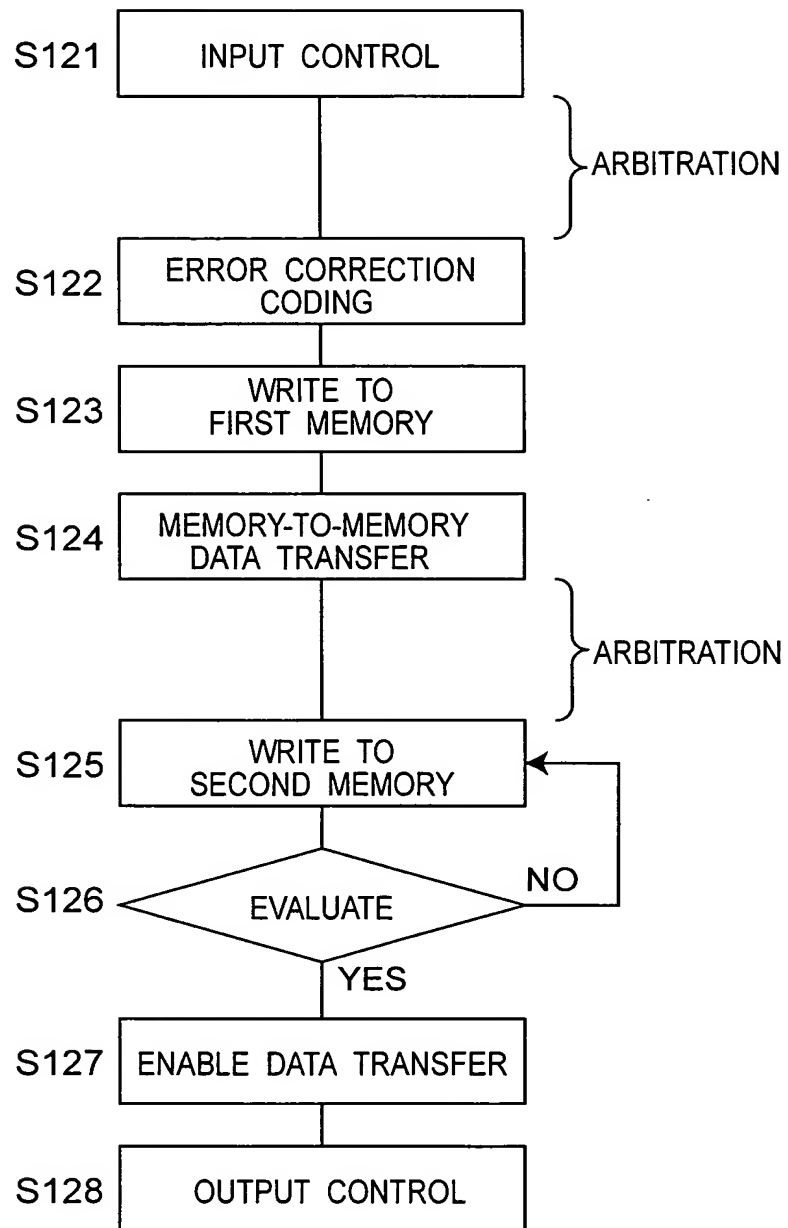


Fig. 11

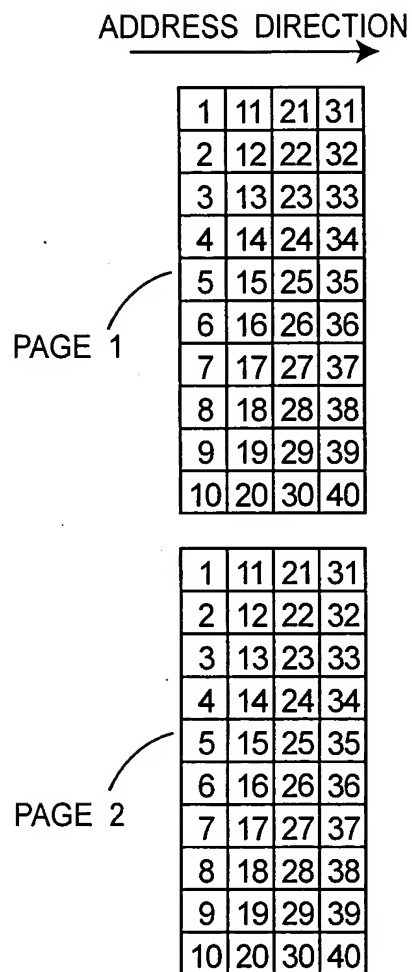


Fig. 12

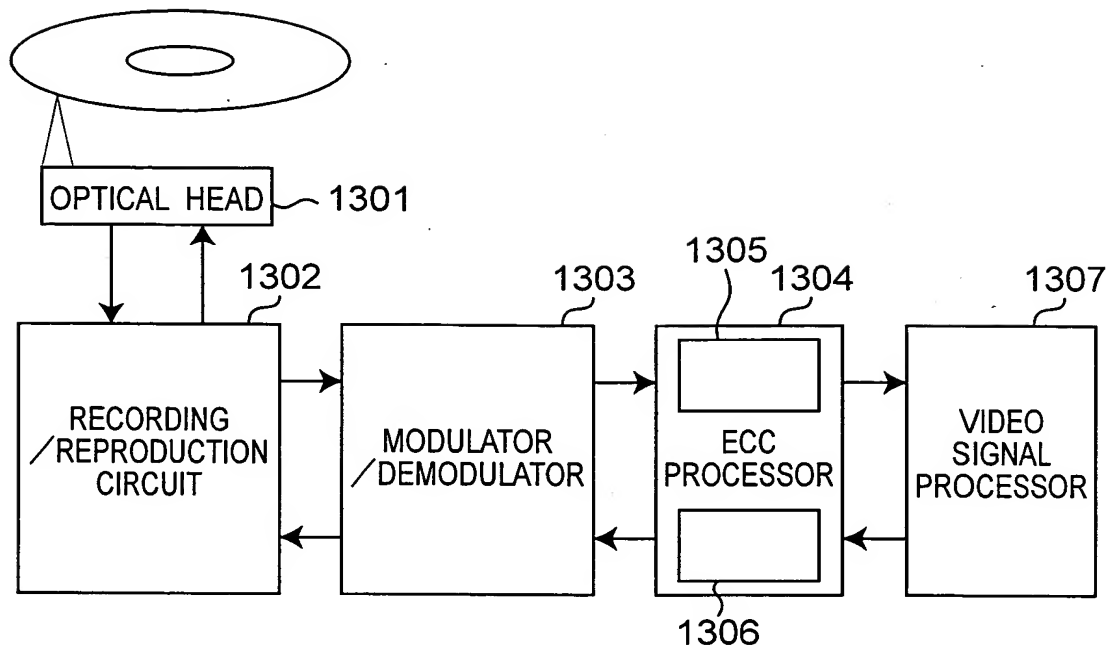


Fig. 13

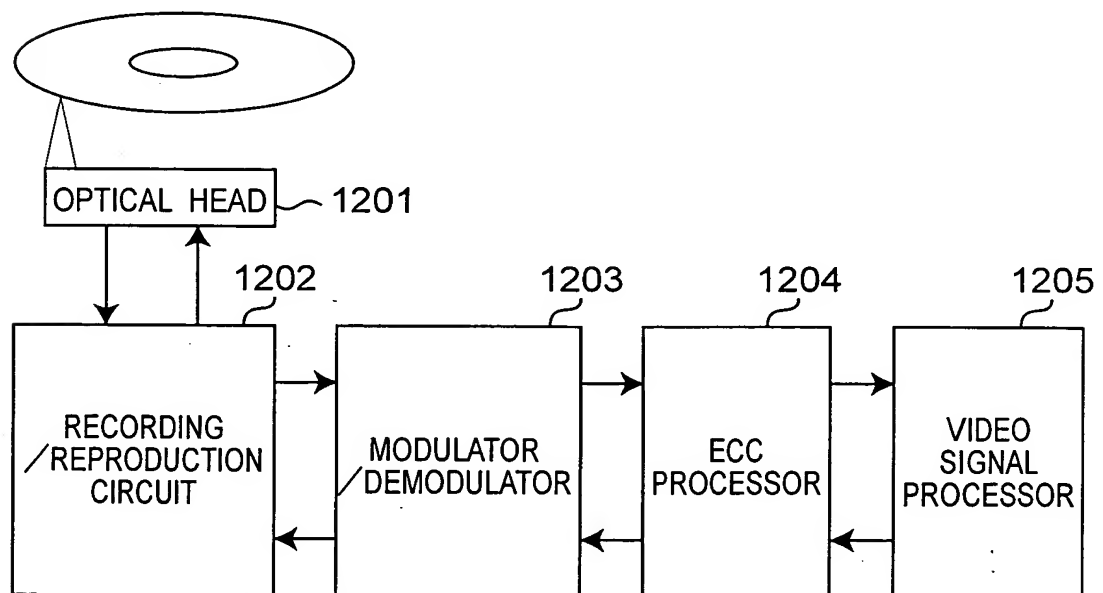


Fig. 14

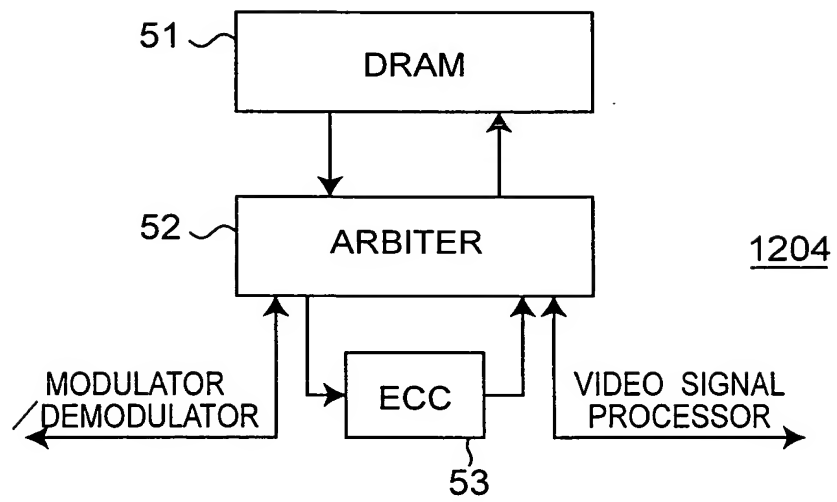


Fig. 15

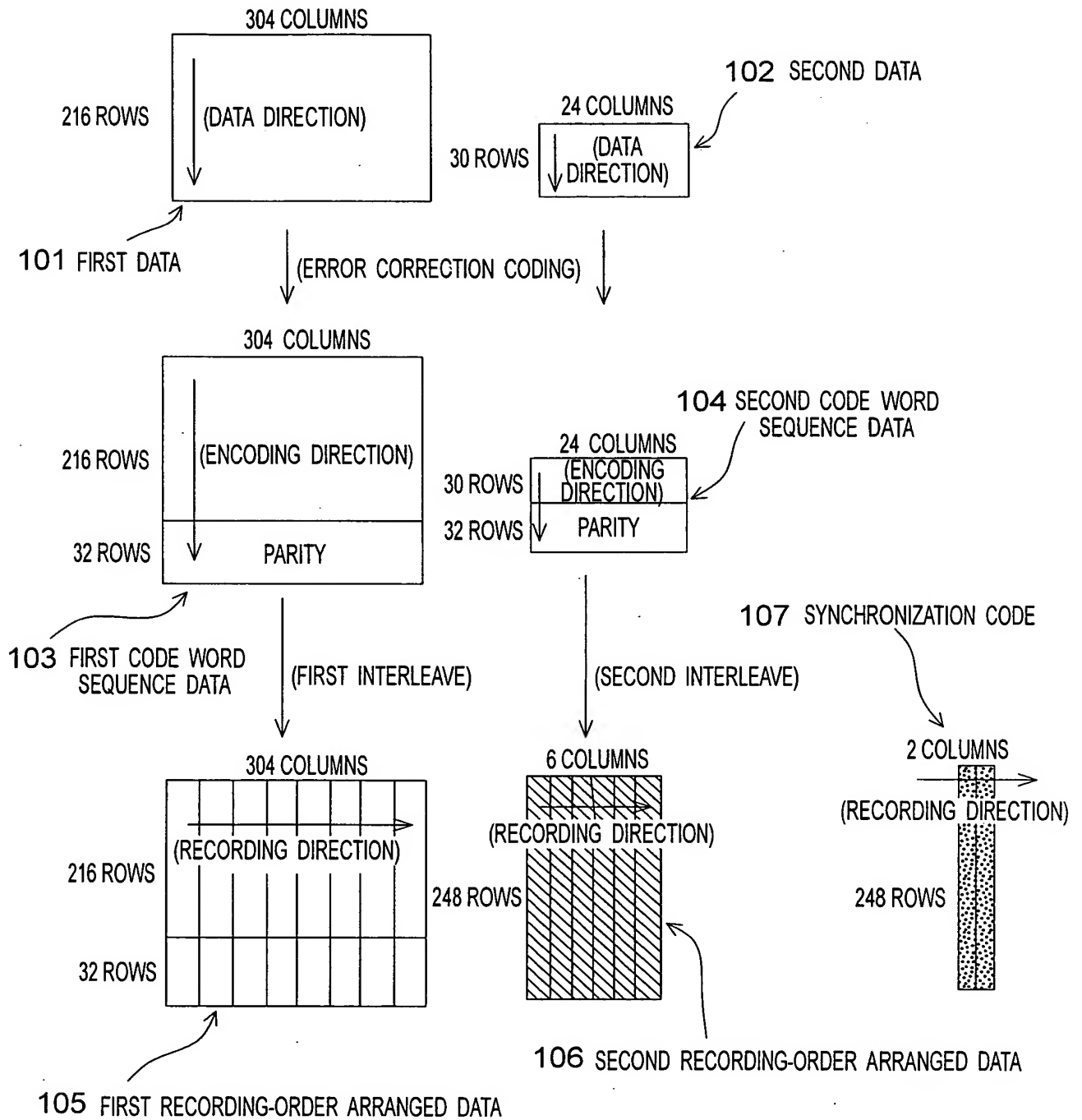


Fig. 16

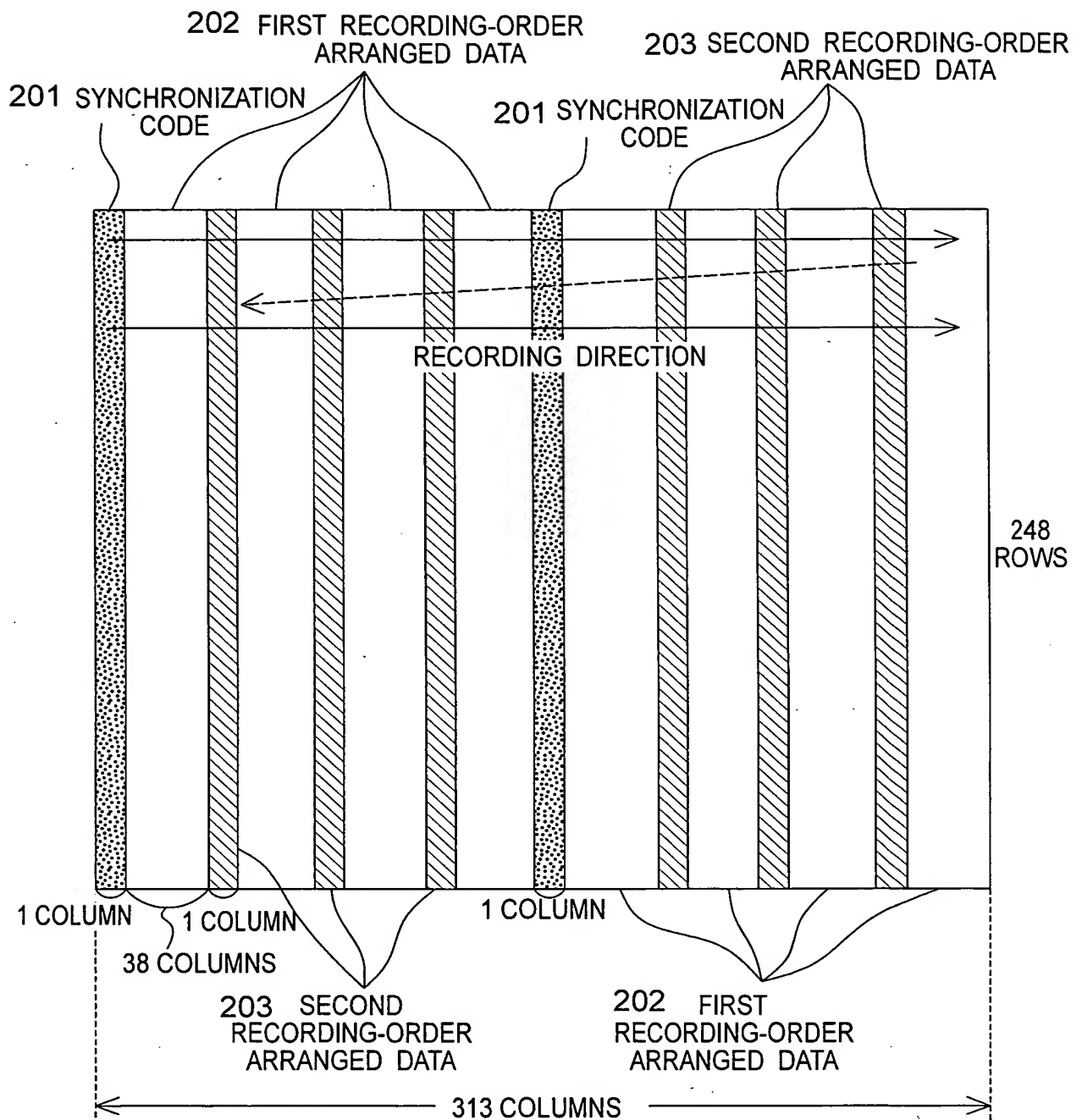


Fig. 17A

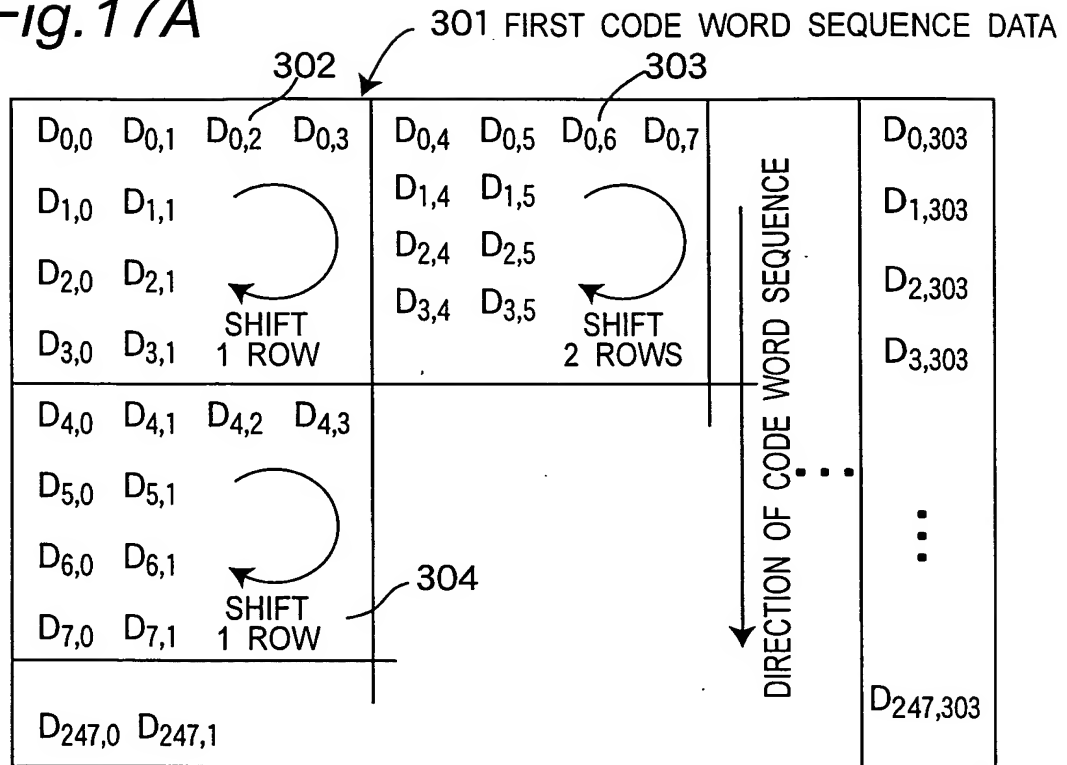


Fig. 17B

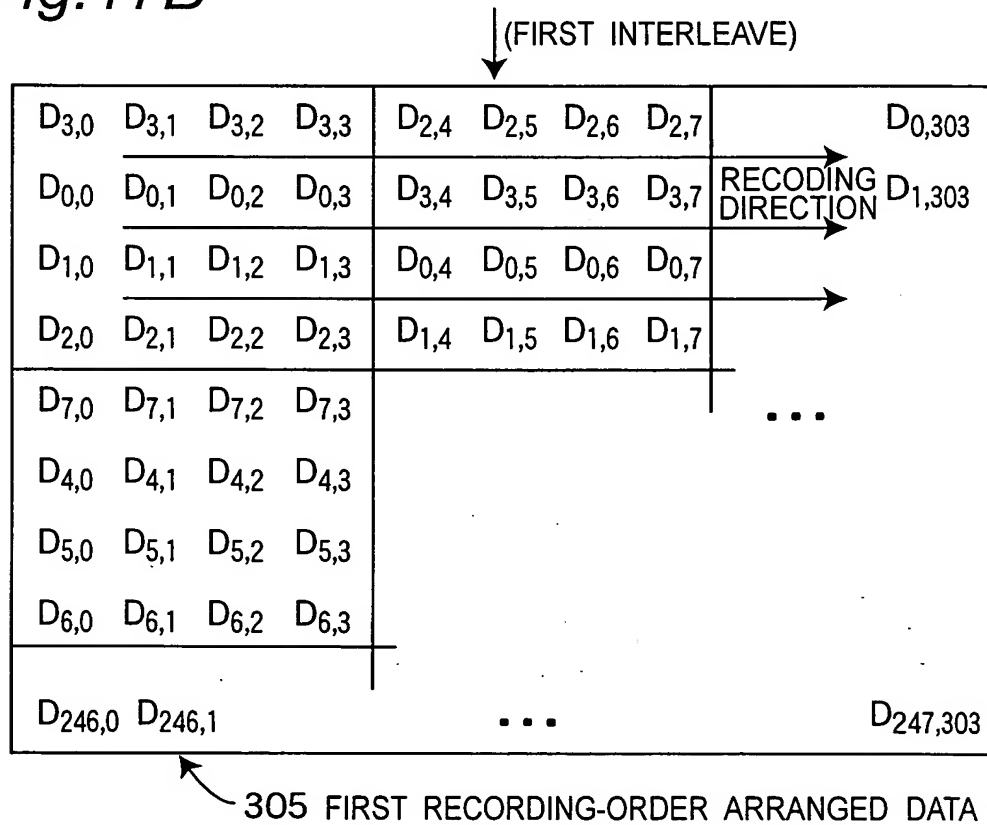


Fig. 18A

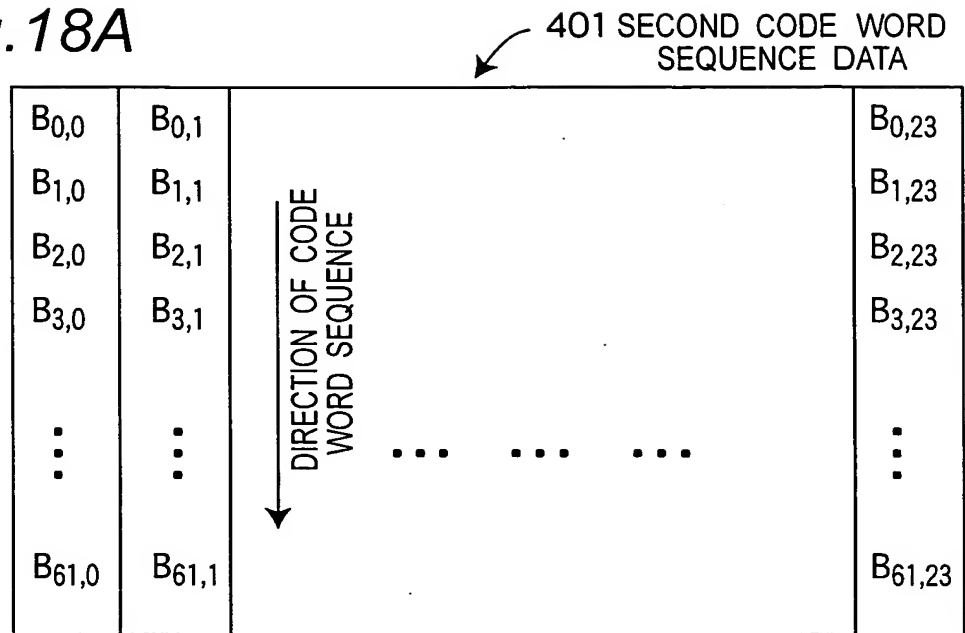


Fig. 18B

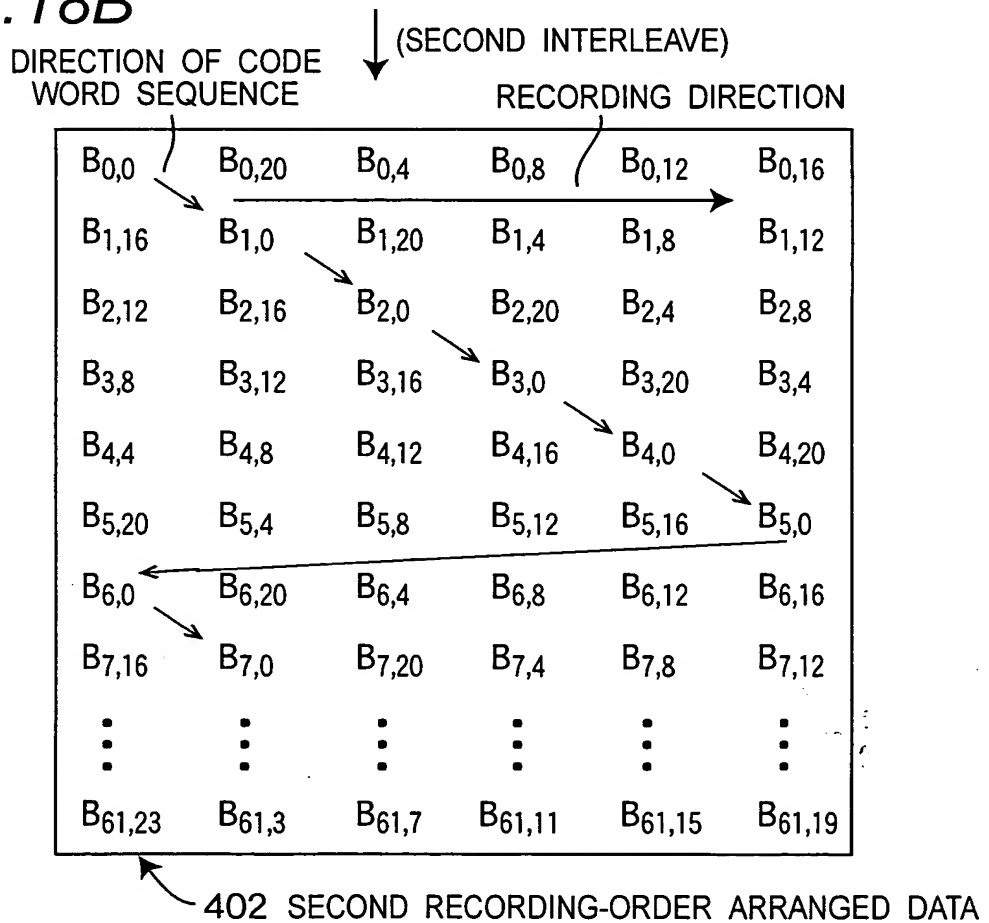


Fig. 19

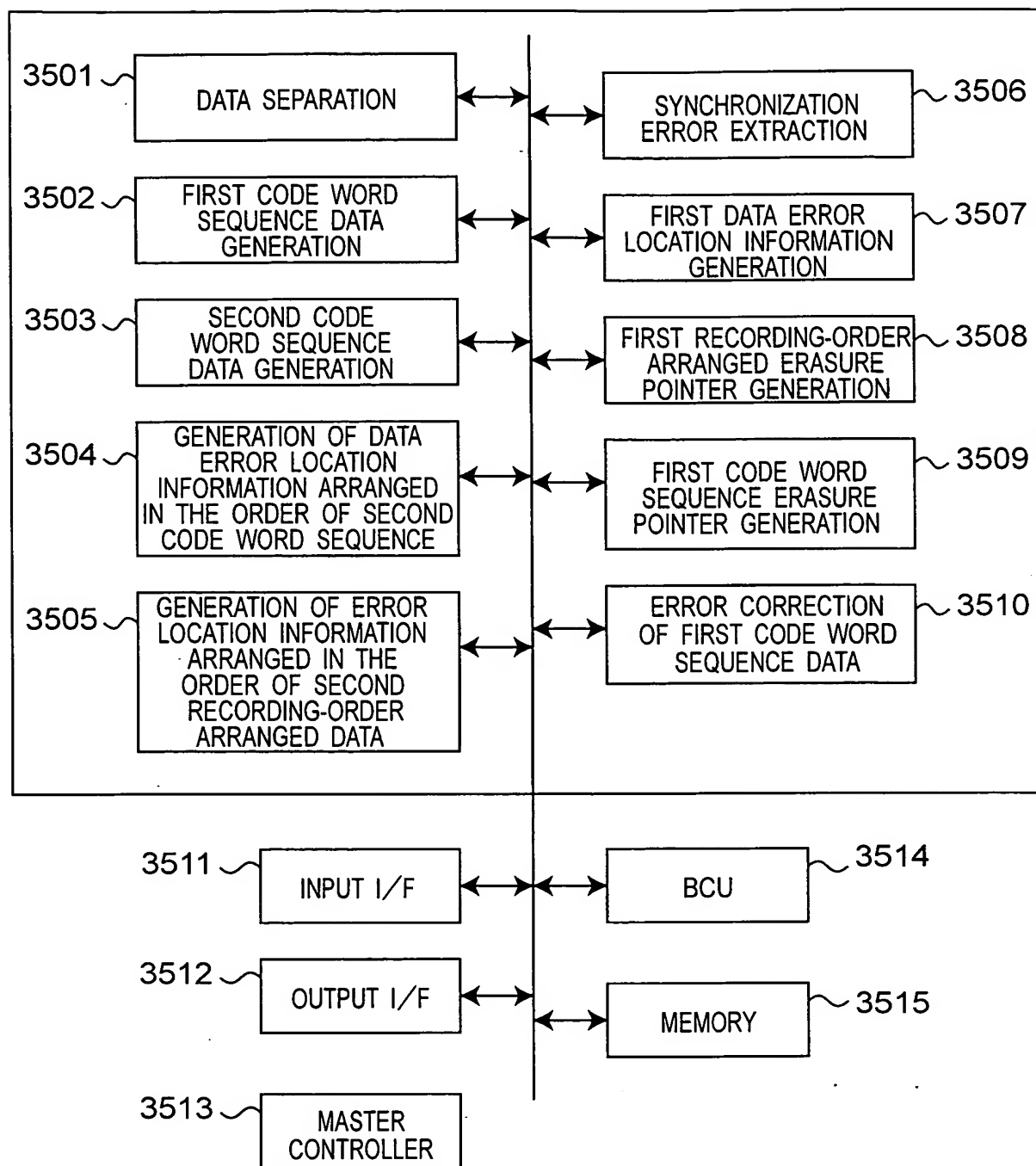


Fig.20

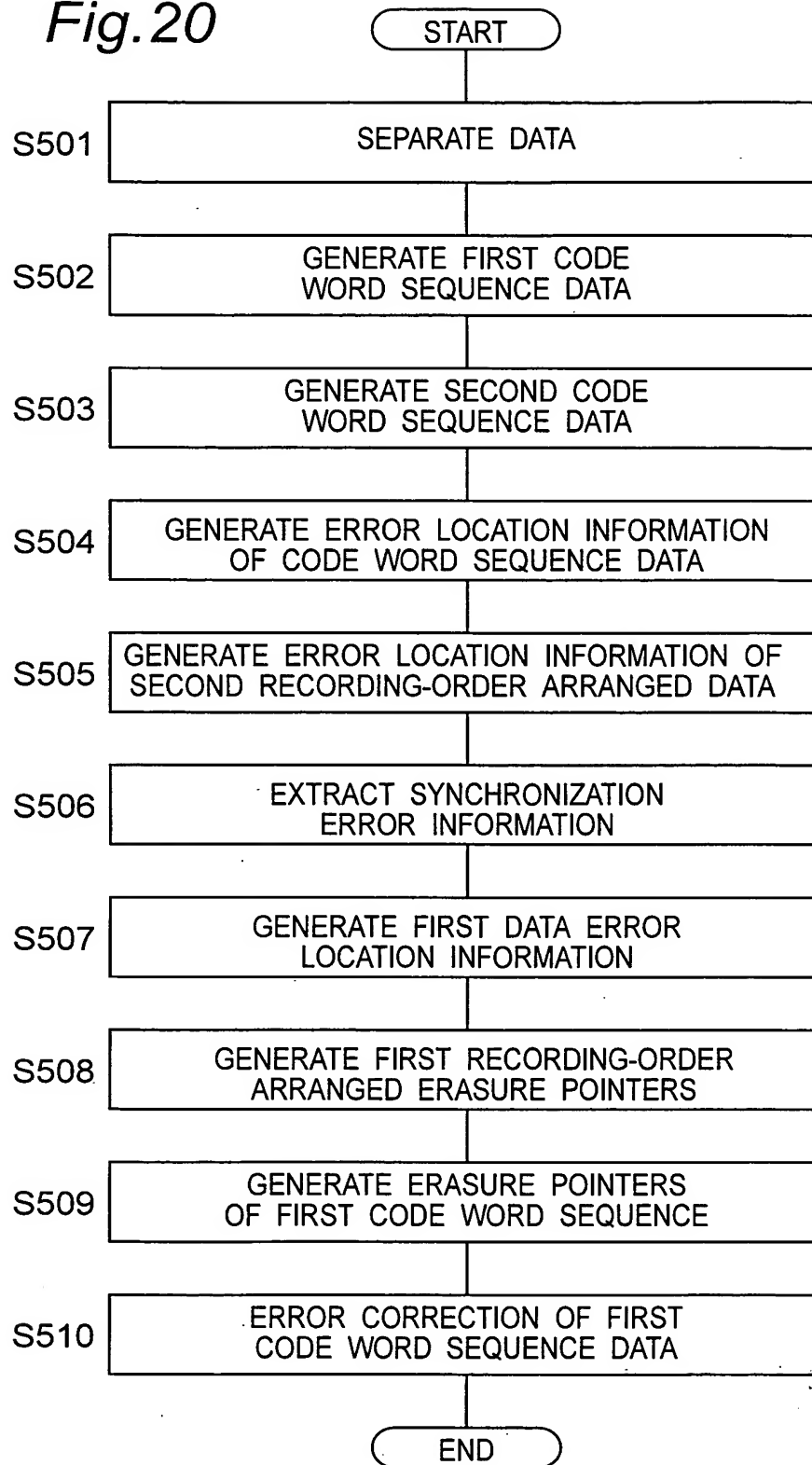


Fig.21

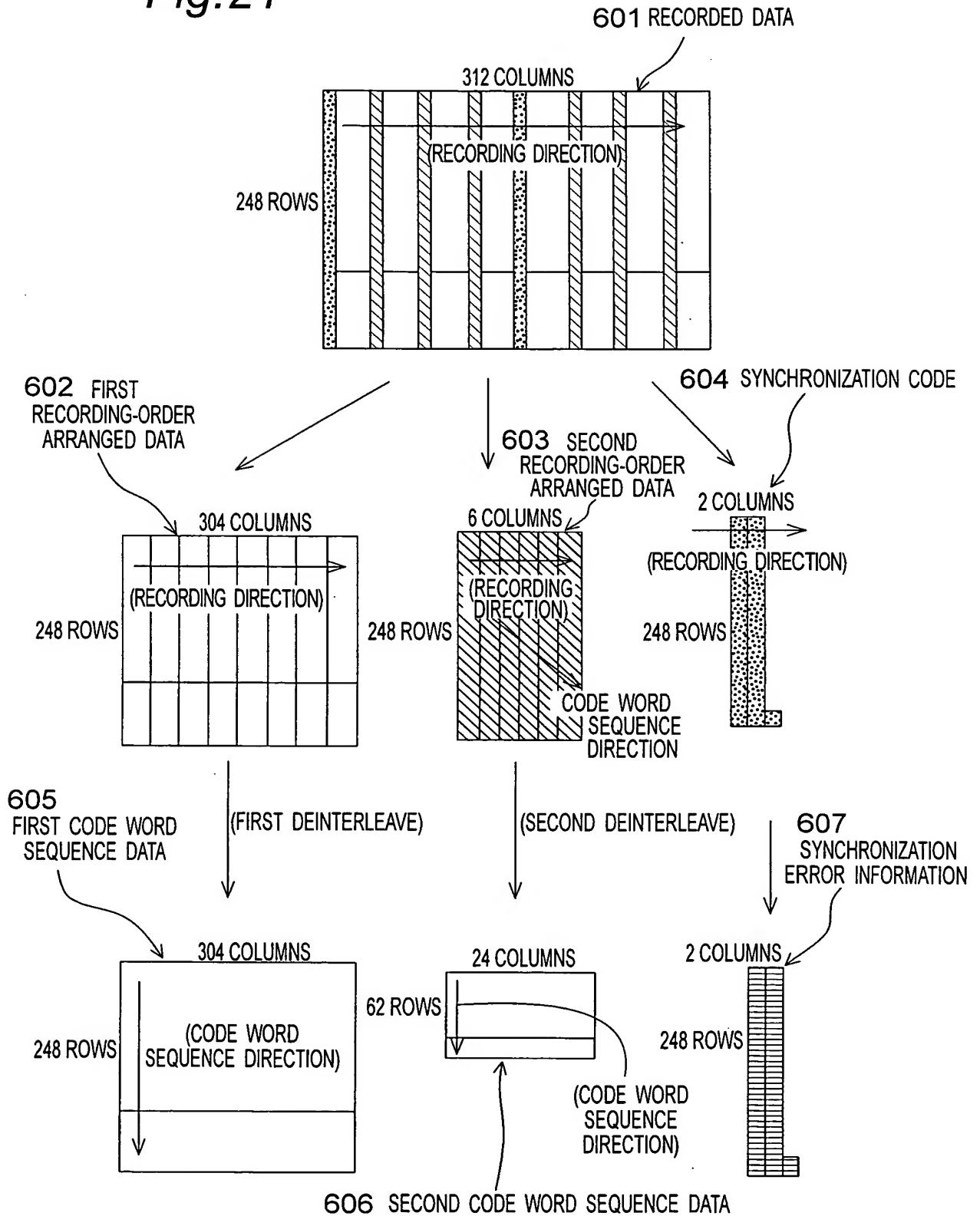


Fig.22

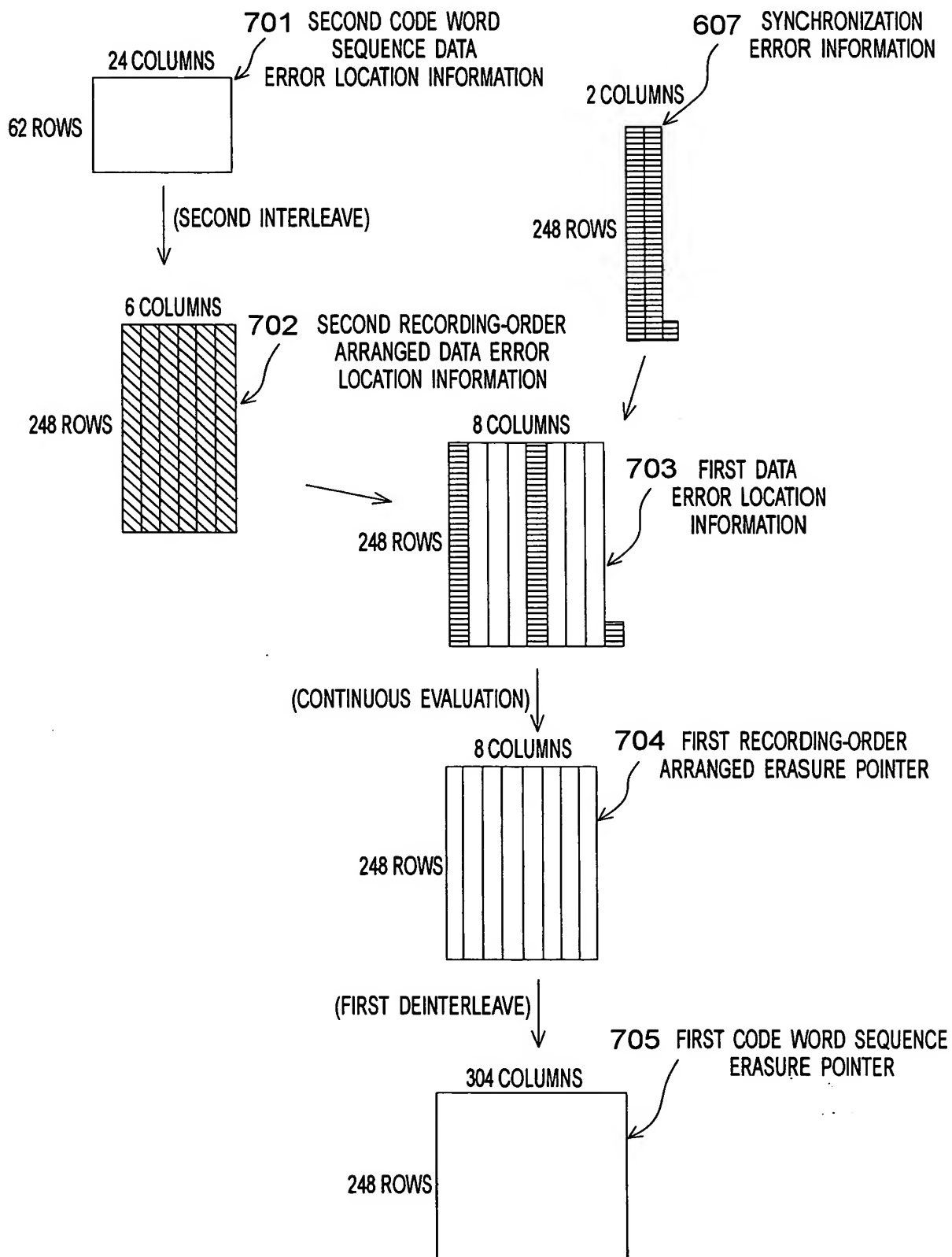


Fig.23

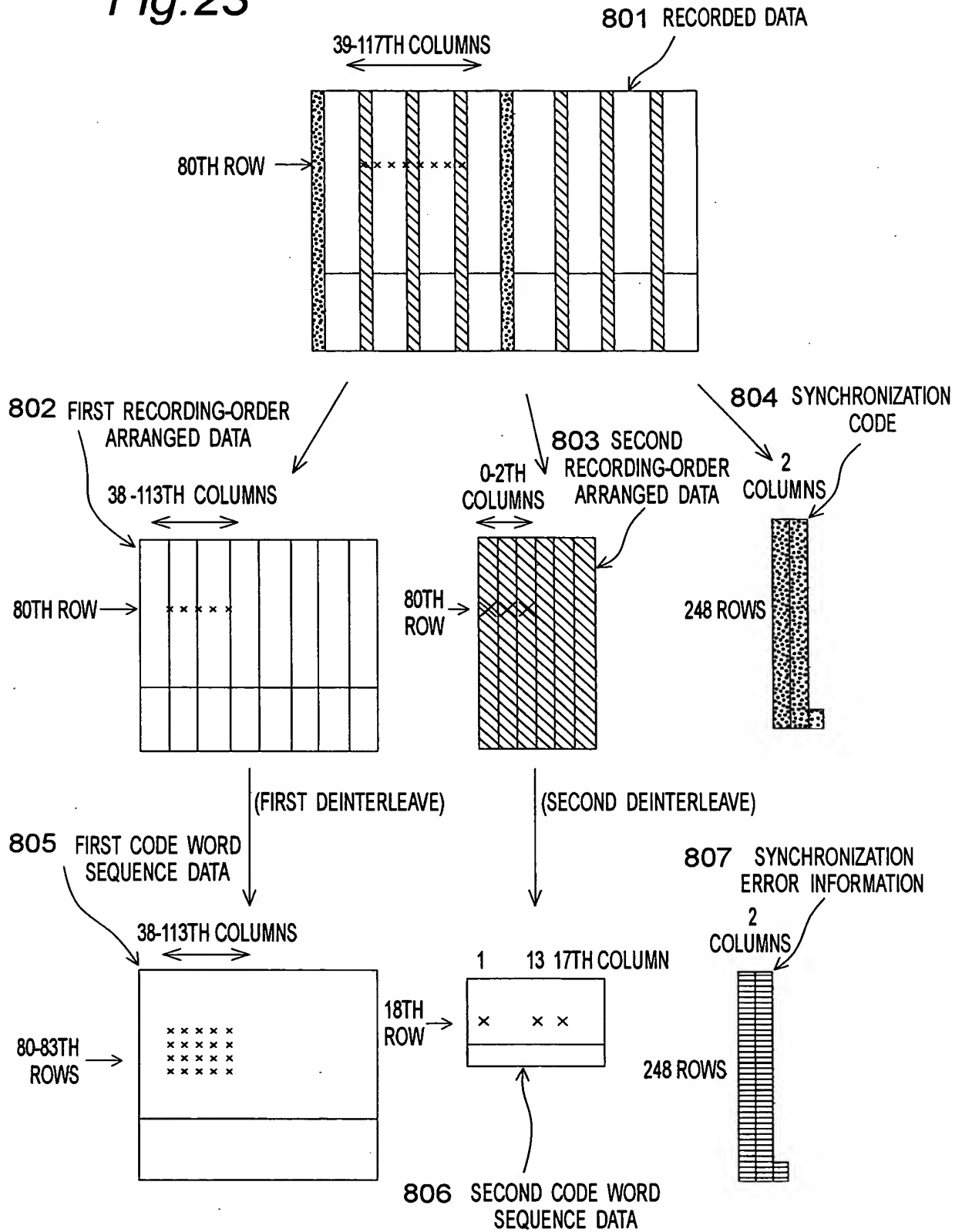


Fig.24

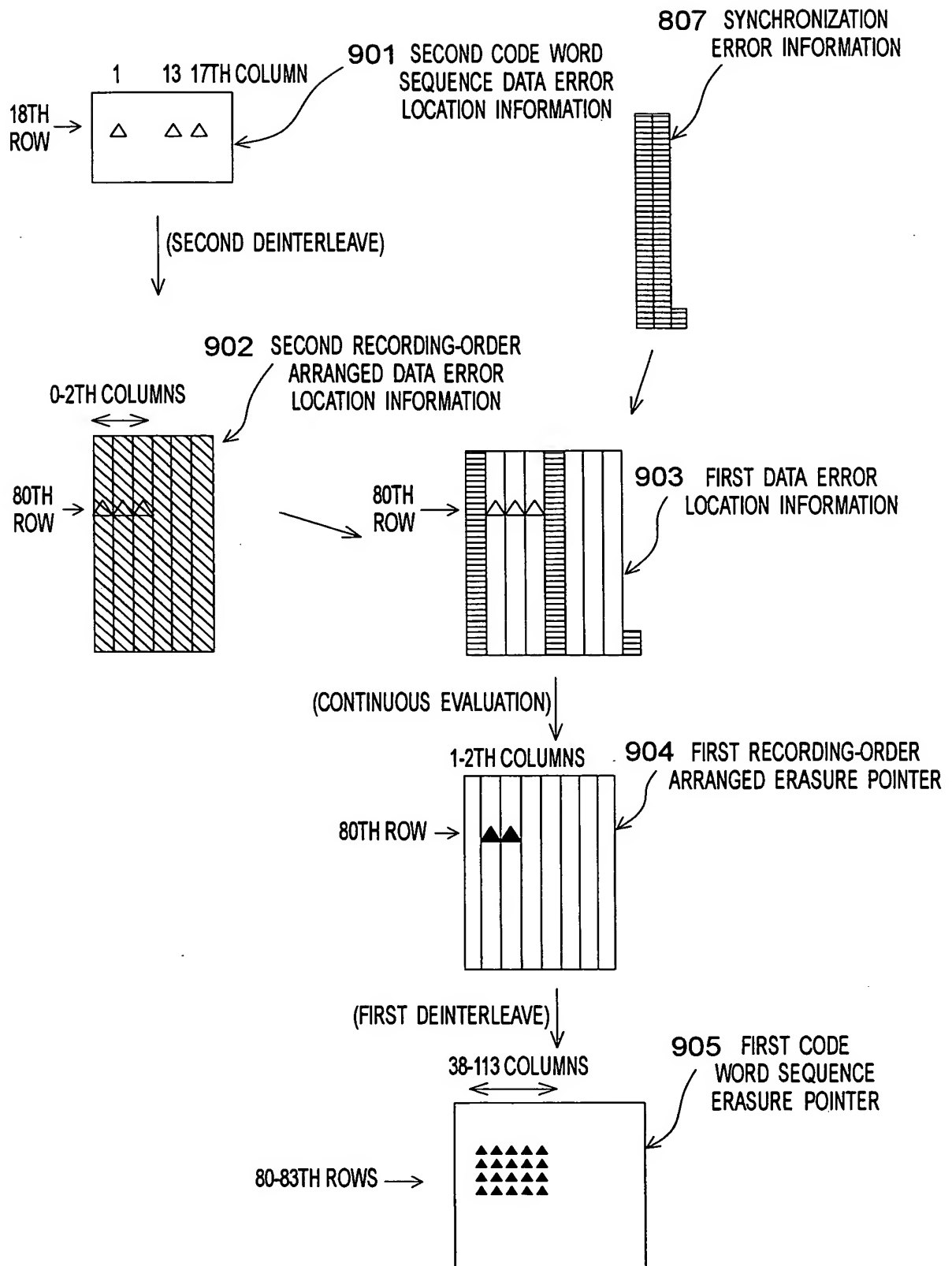


Fig.25

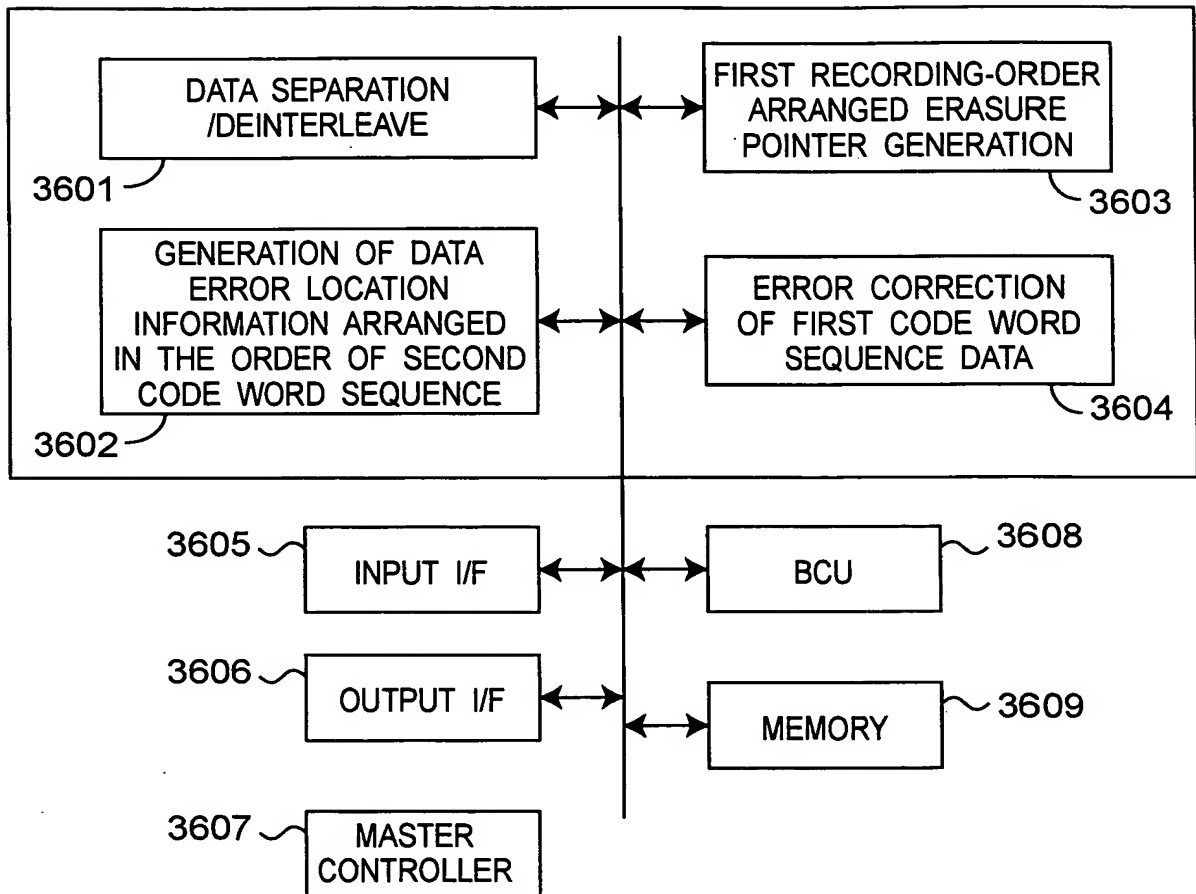


Fig.26

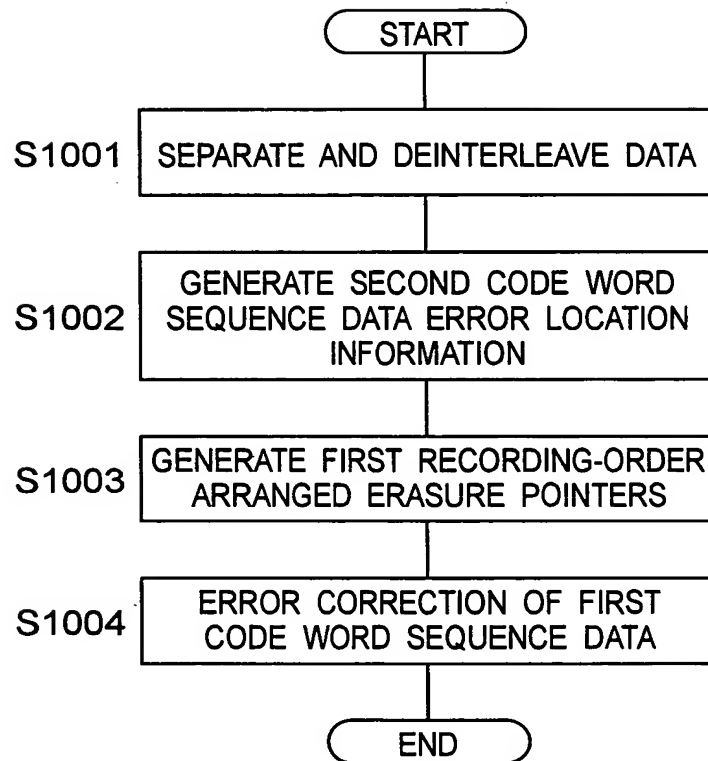


Fig.27

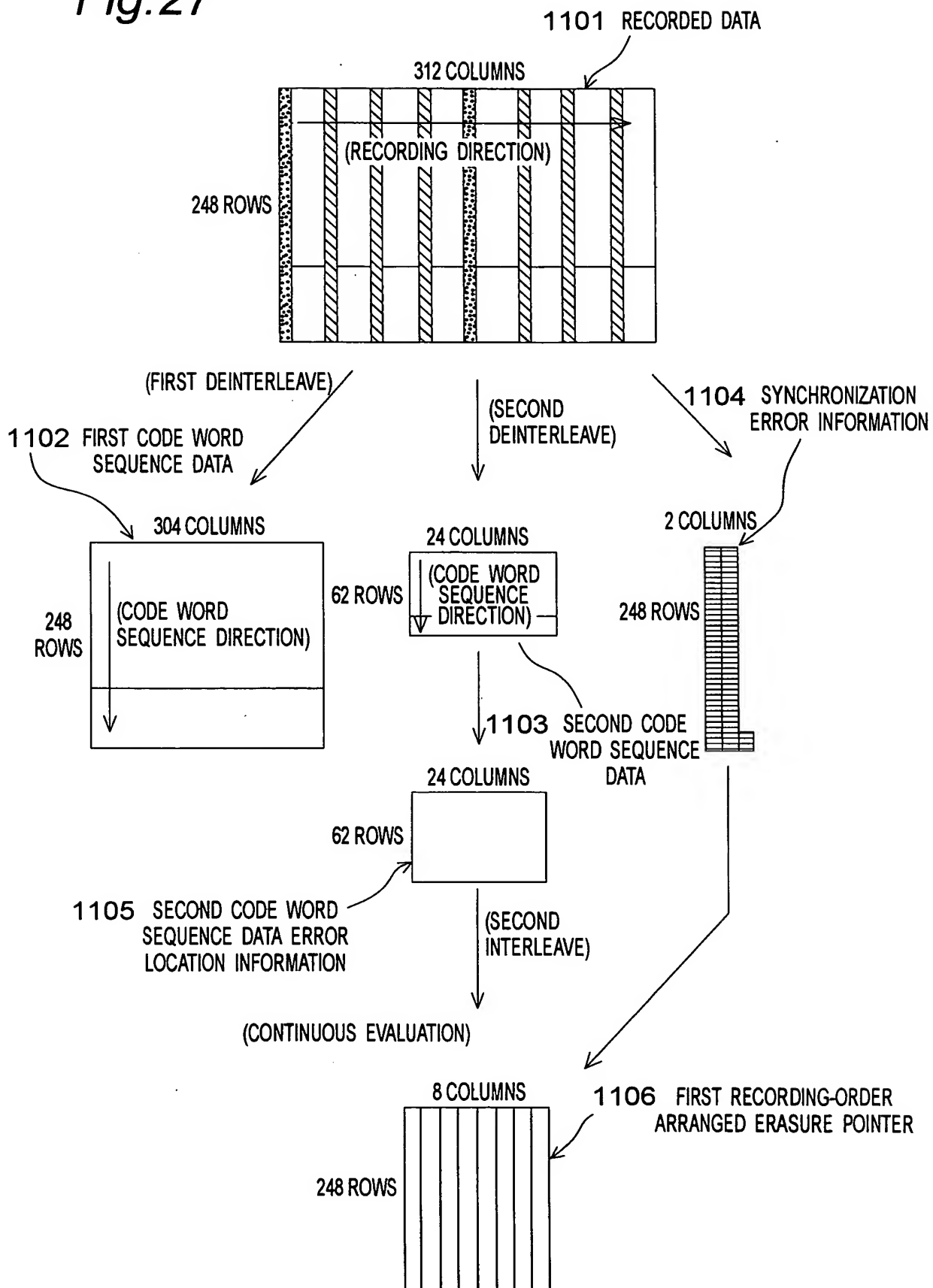


Fig.28

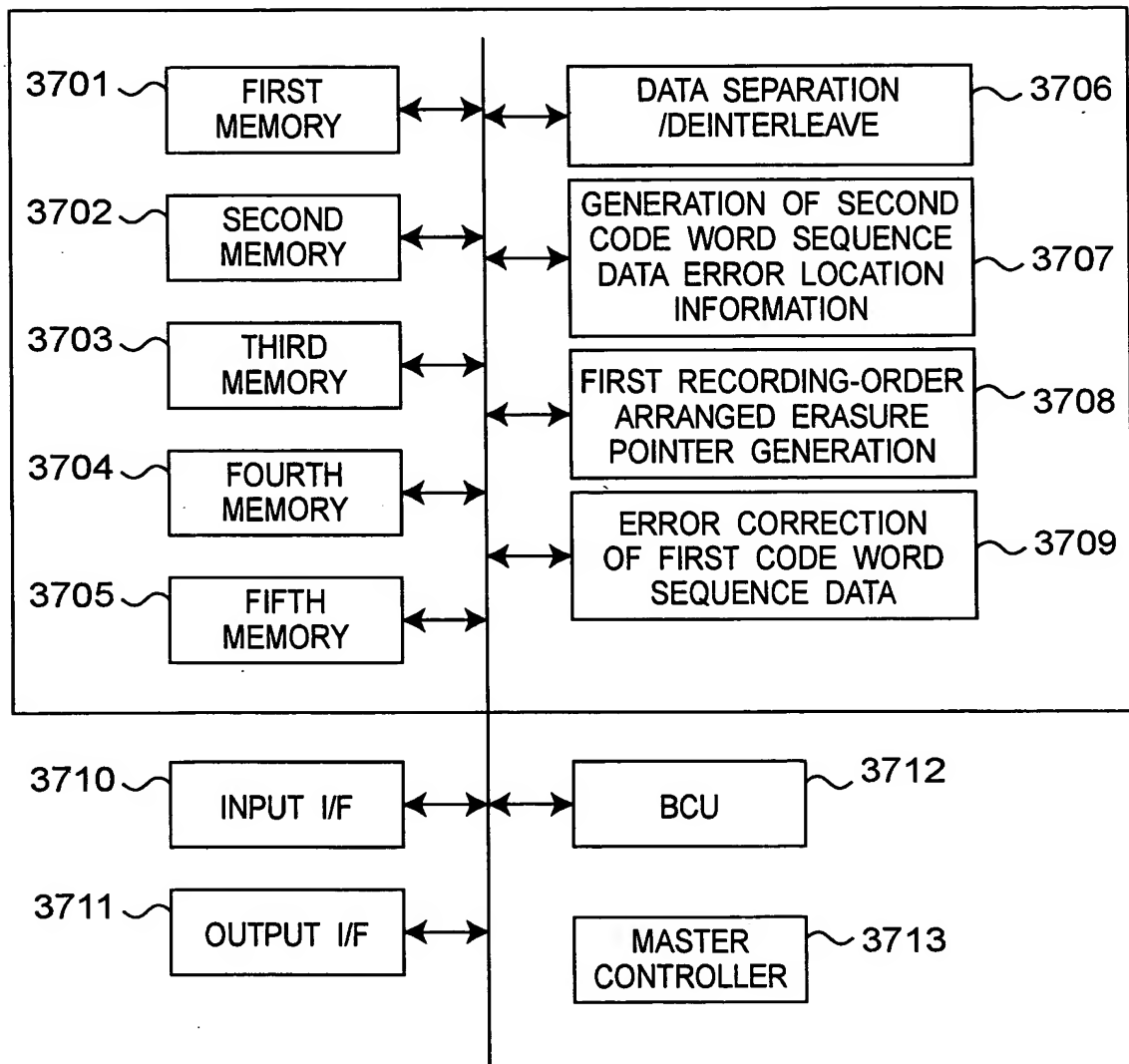


Fig.29

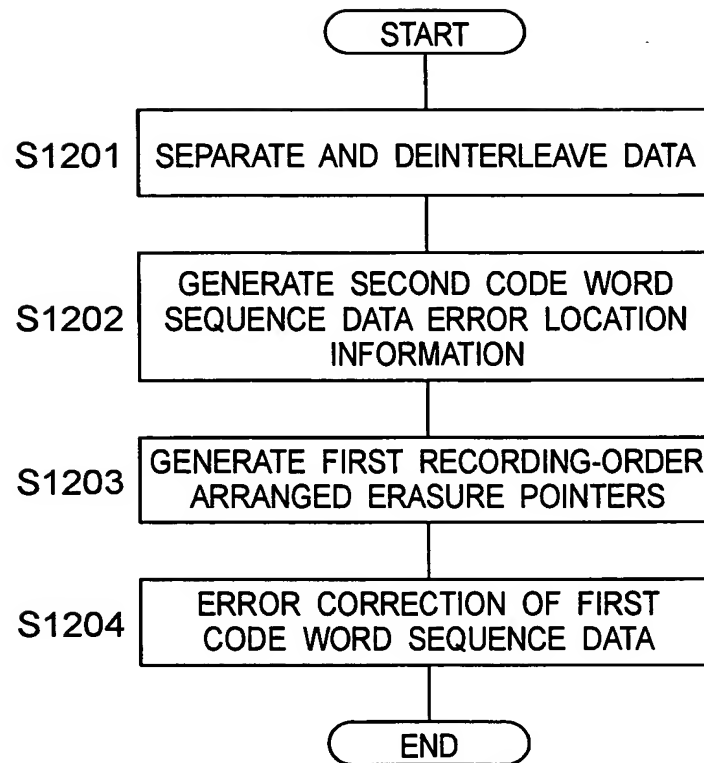


Fig.30

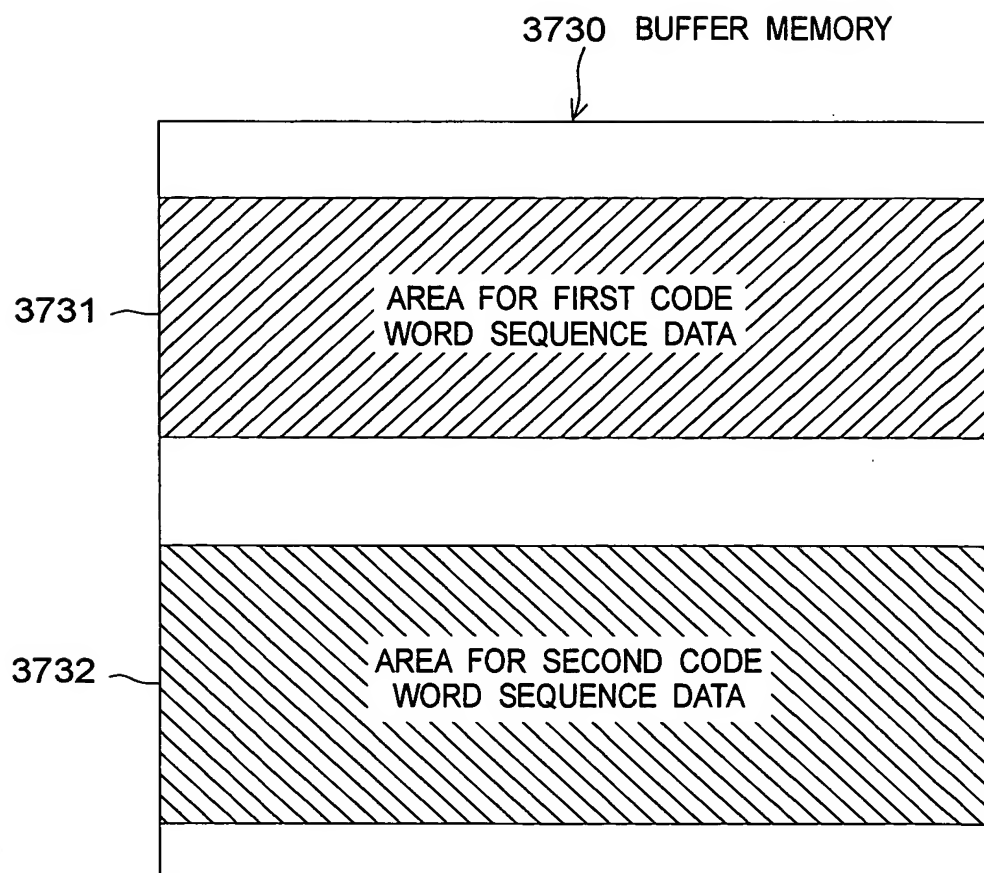


Fig.31

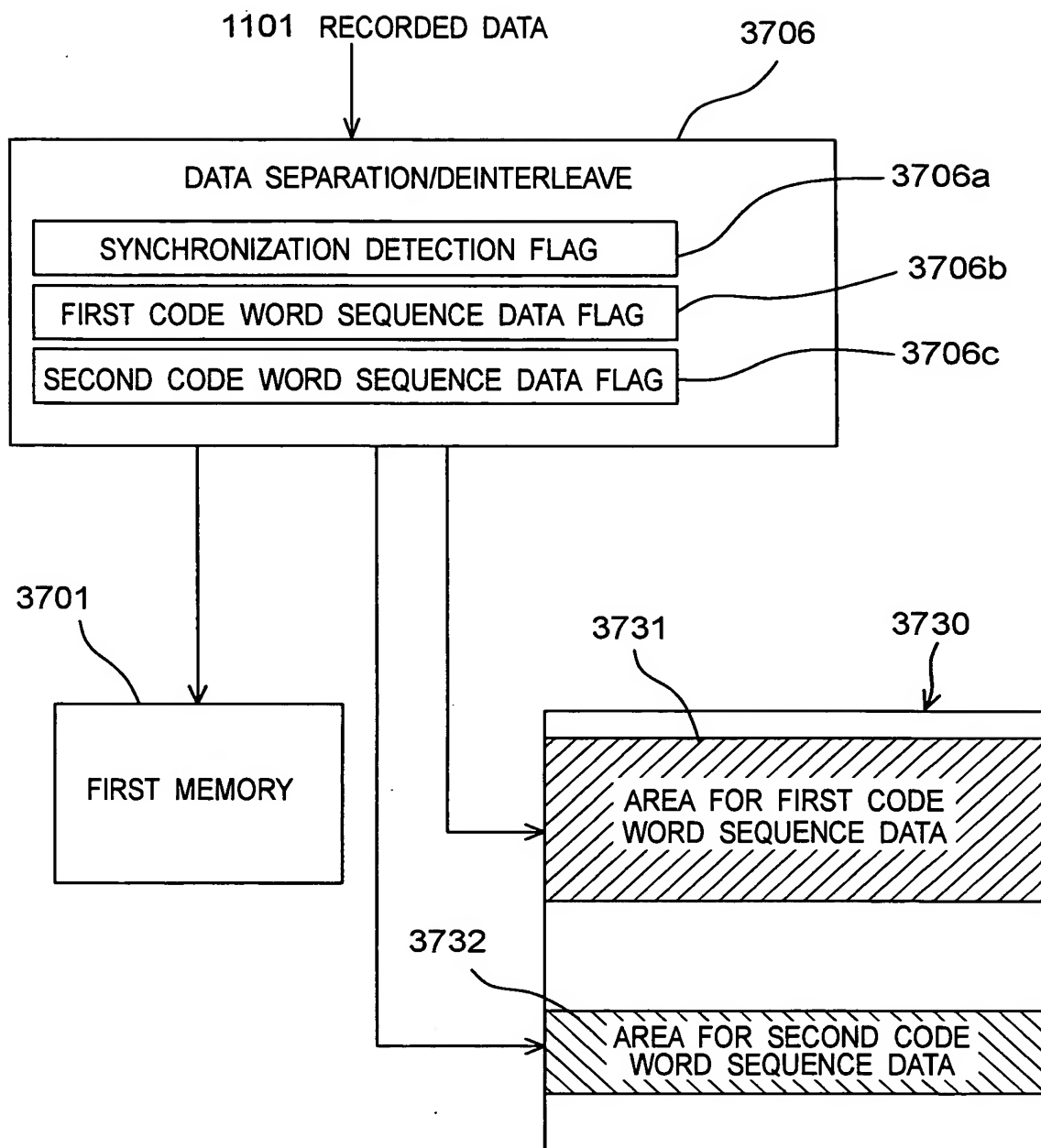


Fig.32

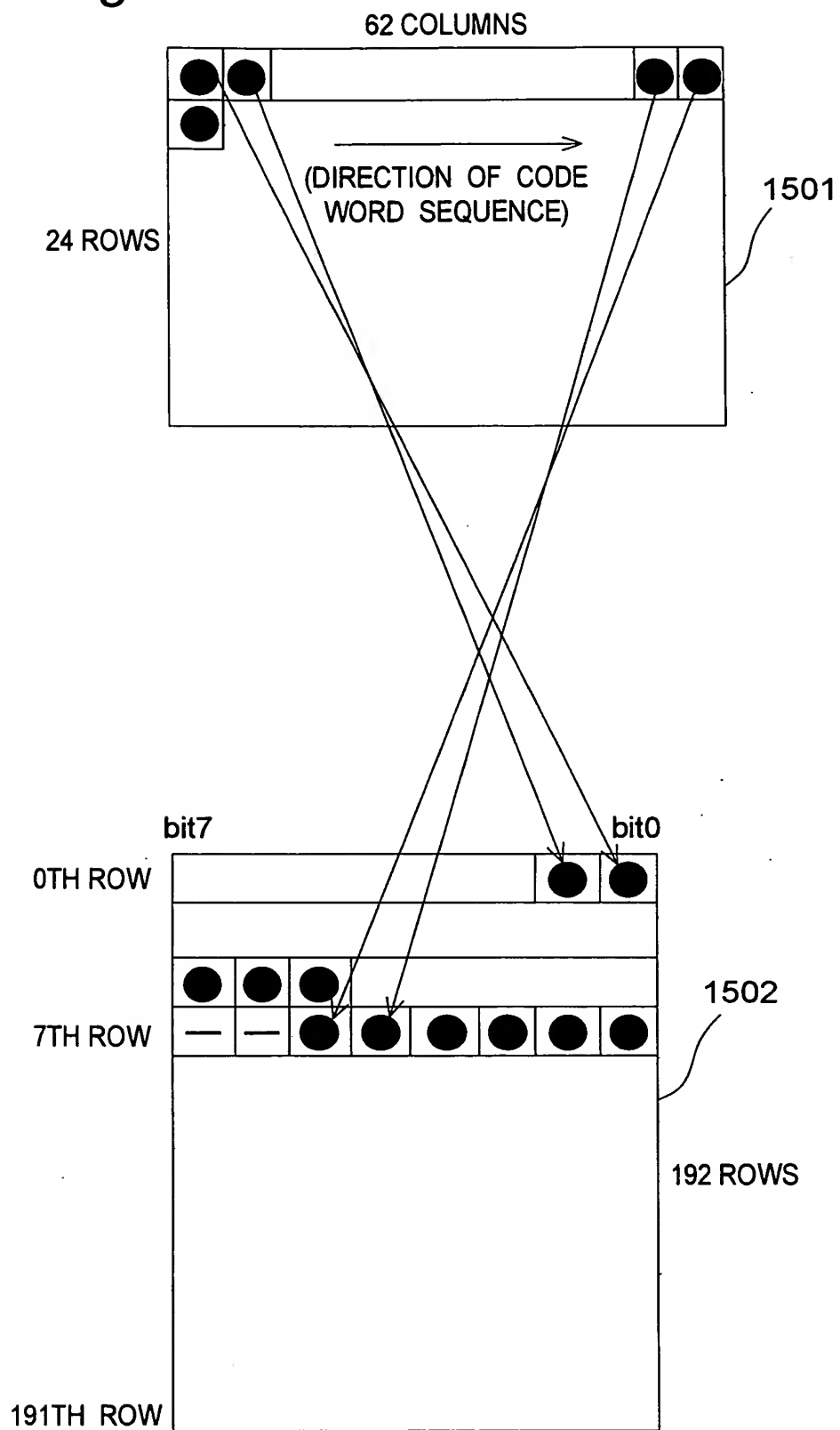


Fig.33A

	SYNCHRONIZATION CODE 1		SYNCHRONIZATION CODE 2	
EXPECTED VALUE	100001010	EXPECTED VALUE	100001010	
DETECTED VALUE	100001010	DETECTED VALUE	101101010	
SYNCHRONIZATION ERROR INFORMATION	0	SYNCHRONIZATION ERROR INFORMATION	1	

Fig.33B

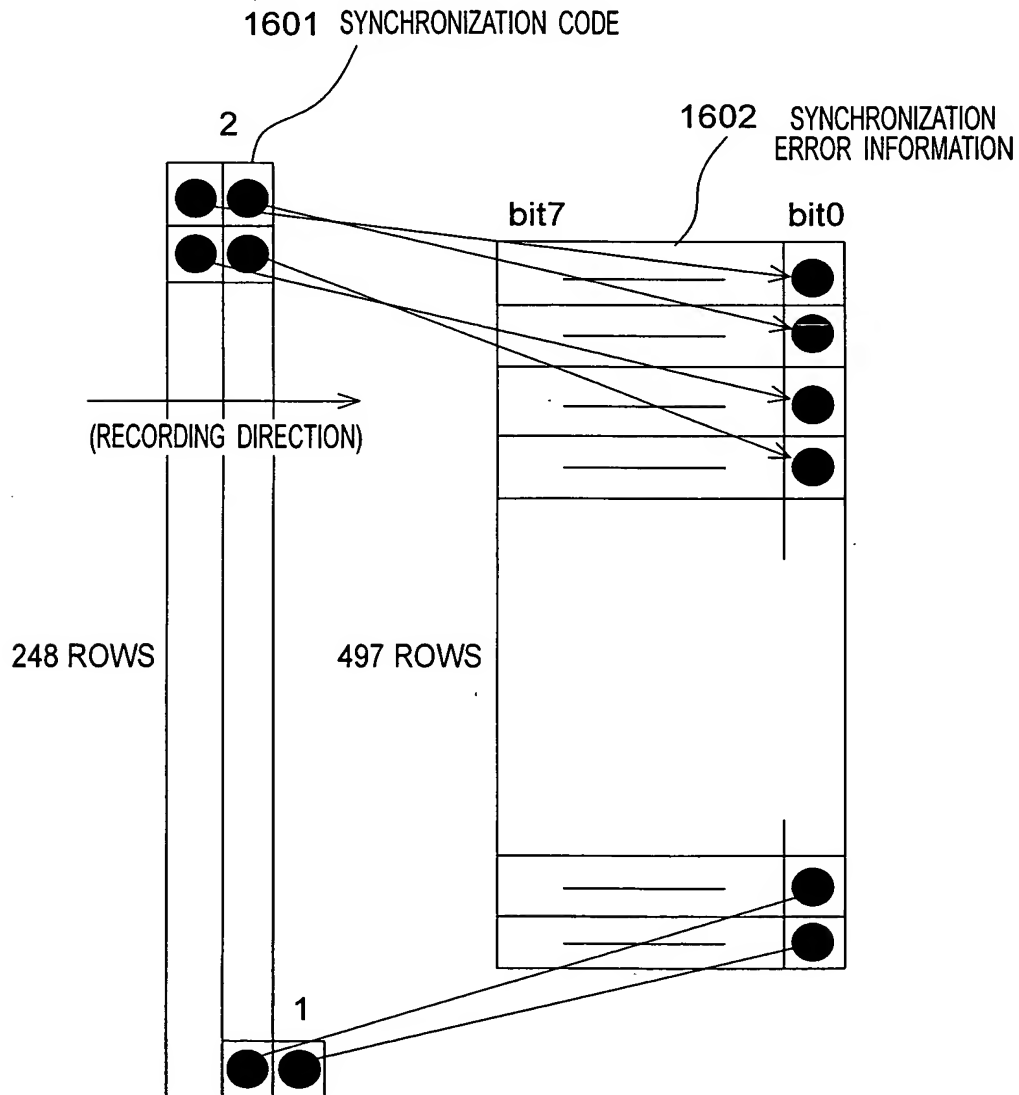


Fig.34

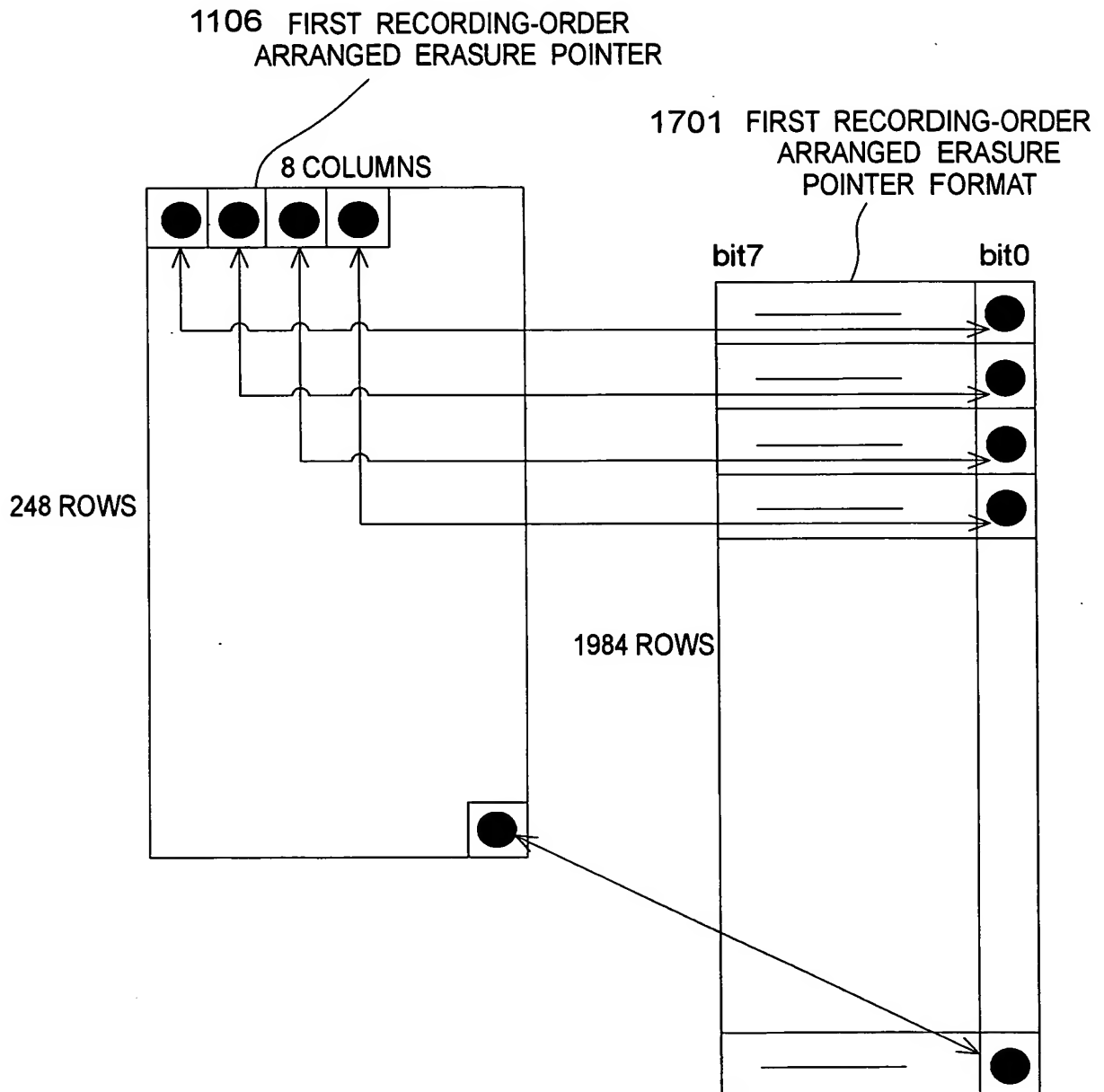


Fig.35

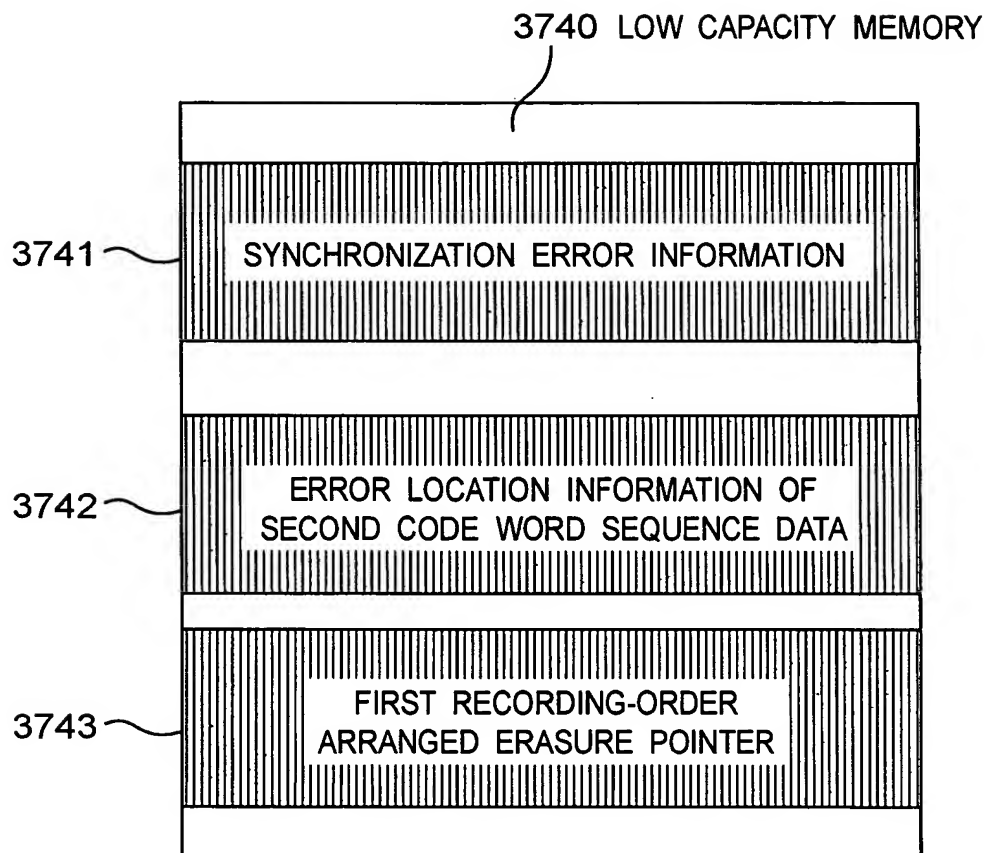
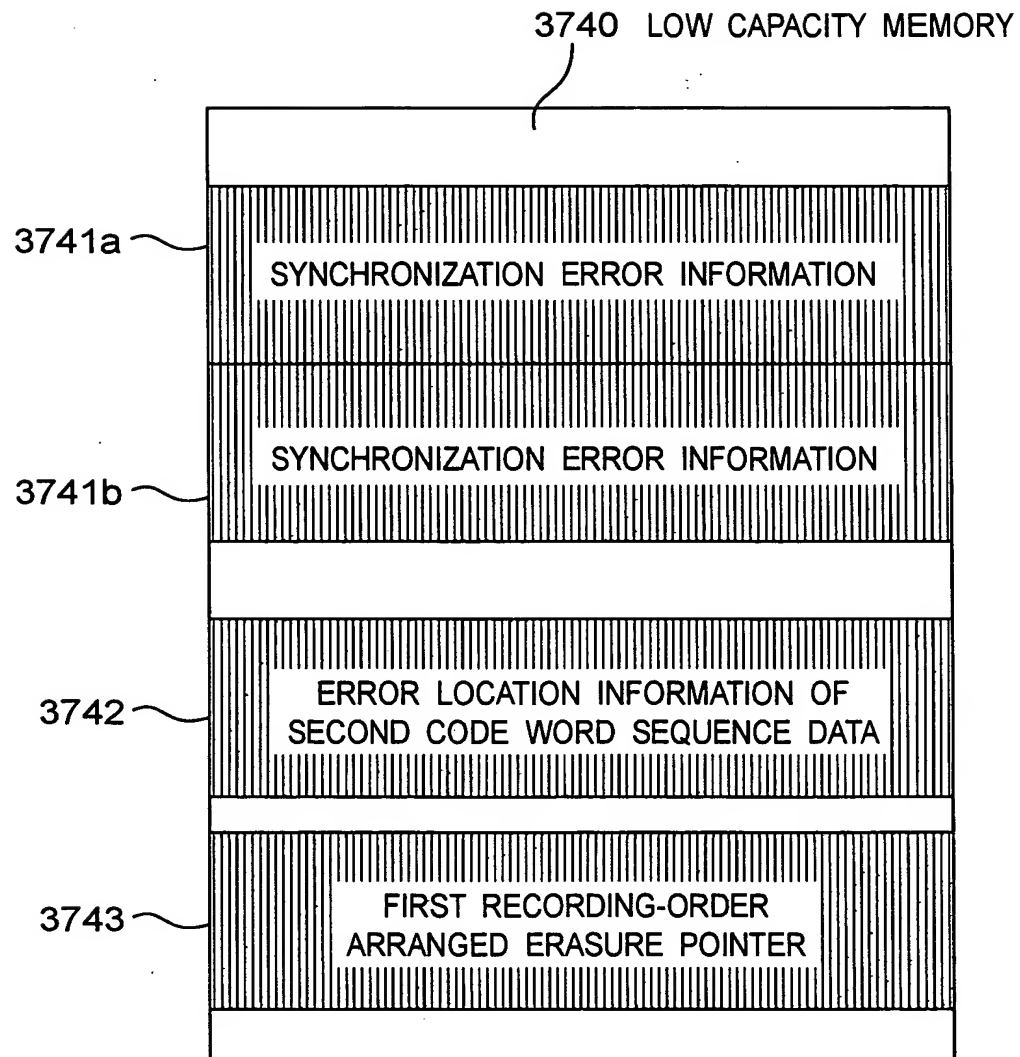


Fig. 36



```
graph TD
    subgraph 3800 [ ]
        direction TB
        subgraph Left [ ]
            3801[DATA SEPARATION]
            3802[GENERATION OF FIRST CODE WORD SEQUENCE DATA SEGMENT]
            3803[FIRST CODE WORD SEQUENCE DATA ASSEMBLY]
            3804[SECOND CODE WORD SEQUENCE DATA GENERATION]
            3805[GENERATION OF DATA ERROR LOCATION INFORMATION ARRANGED IN THE ORDER OF SECOND CODE WORD SEQUENCE]
            3805b[GENERATION OF ERROR LOCATION INFORMATION ARRANGED IN THE ORDER OF SECOND RECORDING-ORDER ARRANGED DATA]
        end
        subgraph Right [ ]
            3806[SYNCHRONIZATION ERROR EXTRACTION]
            3807[FIRST DATA ERROR LOCATION INFORMATION GENERATION]
            3808[FIRST RECORDING-ORDER ARRANGED ERASURE POINTER GENERATION]
            3809[FIRST CODE WORD SEQUENCE ERASURE POINTER GENERATION]
            3810[ERROR CORRECTION OF FIRST CODE WORD SEQUENCE DATA]
        end
        3801 <--> 3806
        3802 <--> 3807
        3803 <--> 3808
        3804 <--> 3809
        3805 <--> 3810
        3805b <--> 3810
    end

    3811[INPUT I/F] <--> 3814[BCU]
    3812[OUTPUT I/F] <--> 3815[MEMORY]
    3813[MASTER CONTROLLER] <--> 3814
    3813 <--> 3815
```

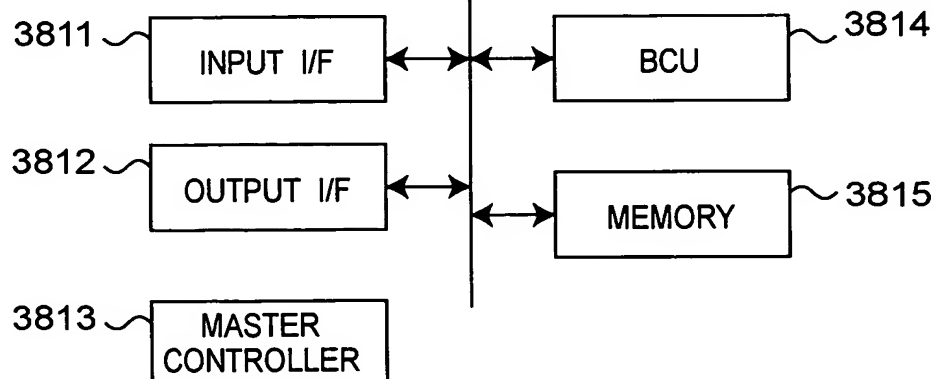


Fig.38

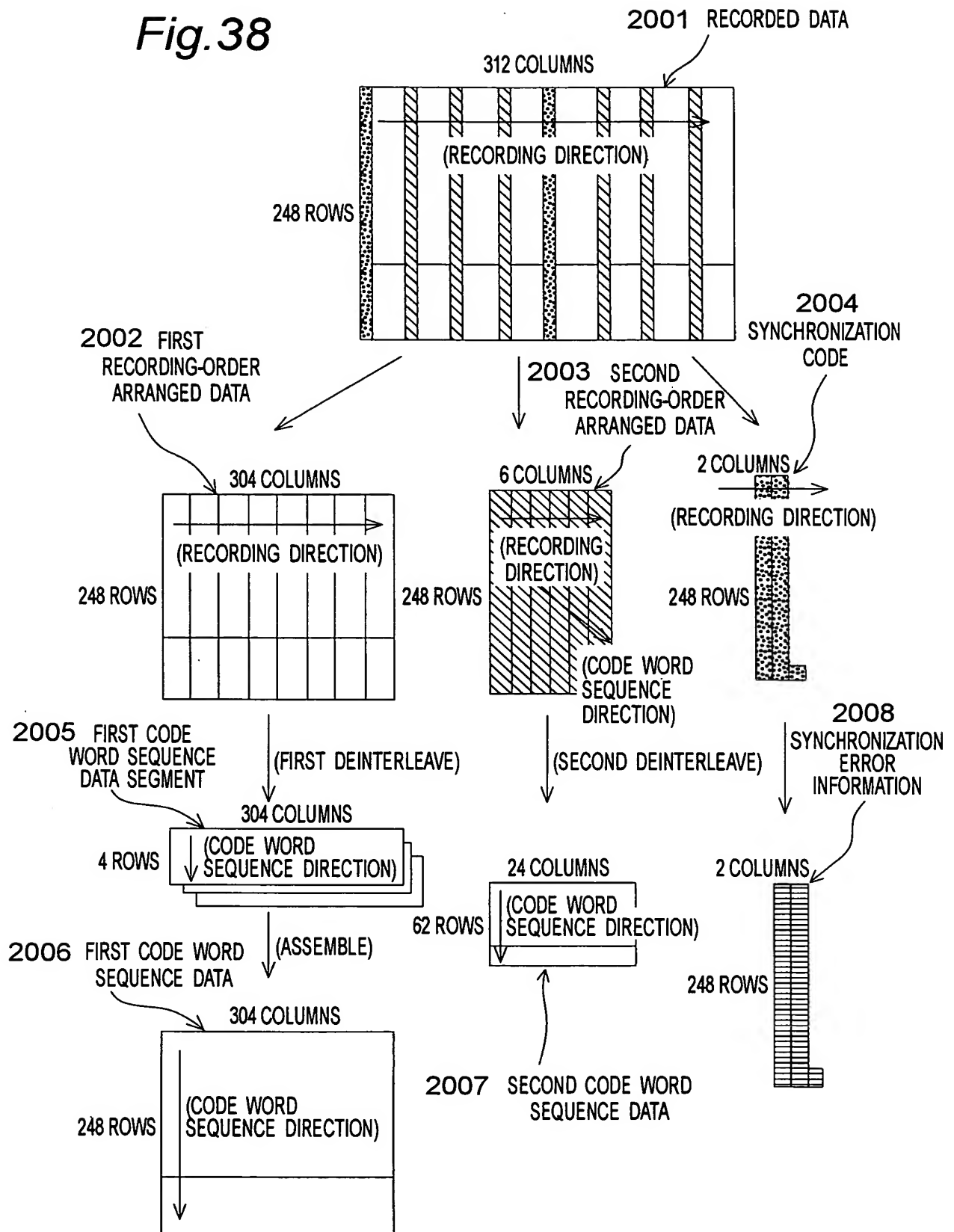


Fig. 39

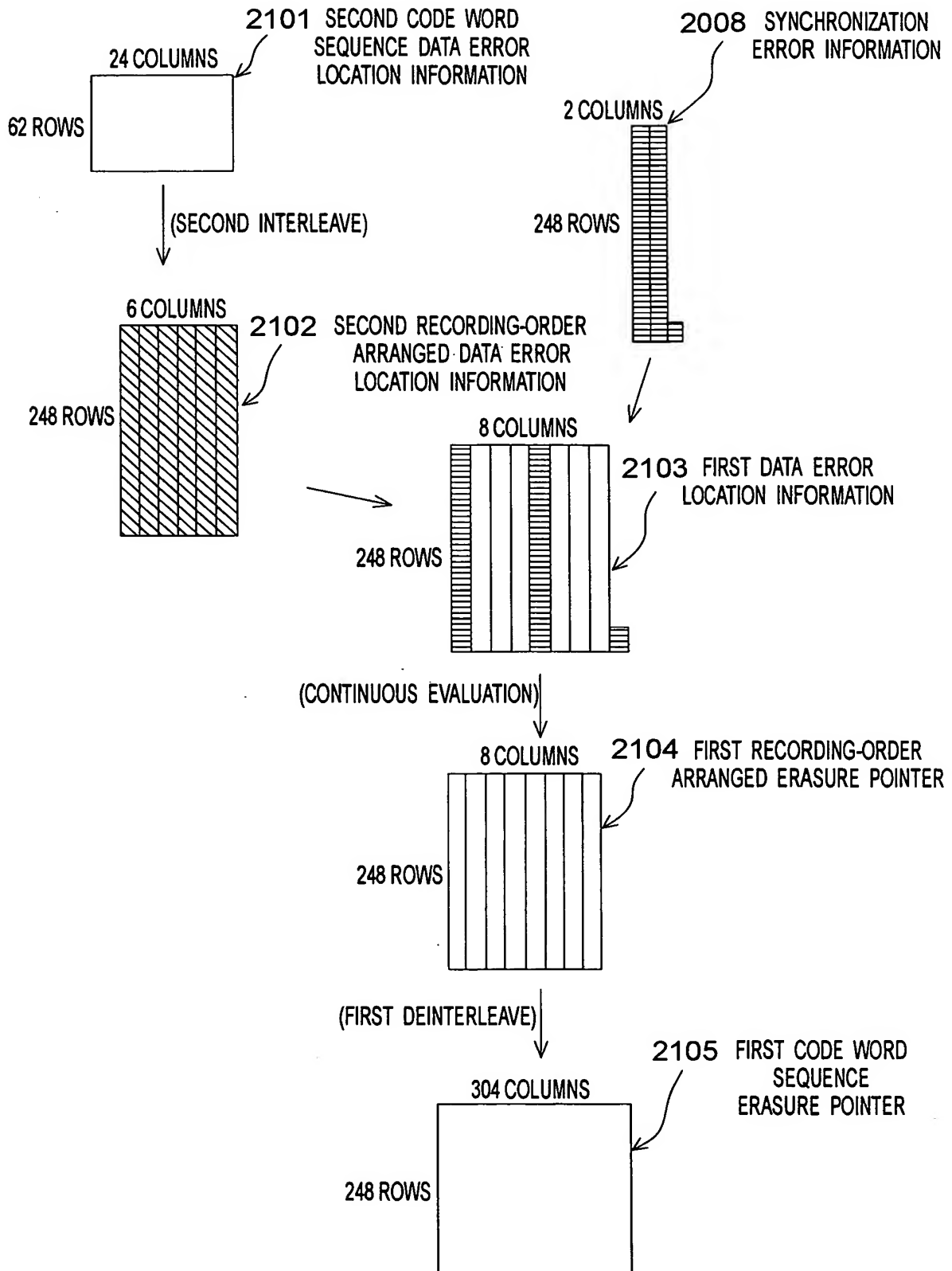


Fig.40

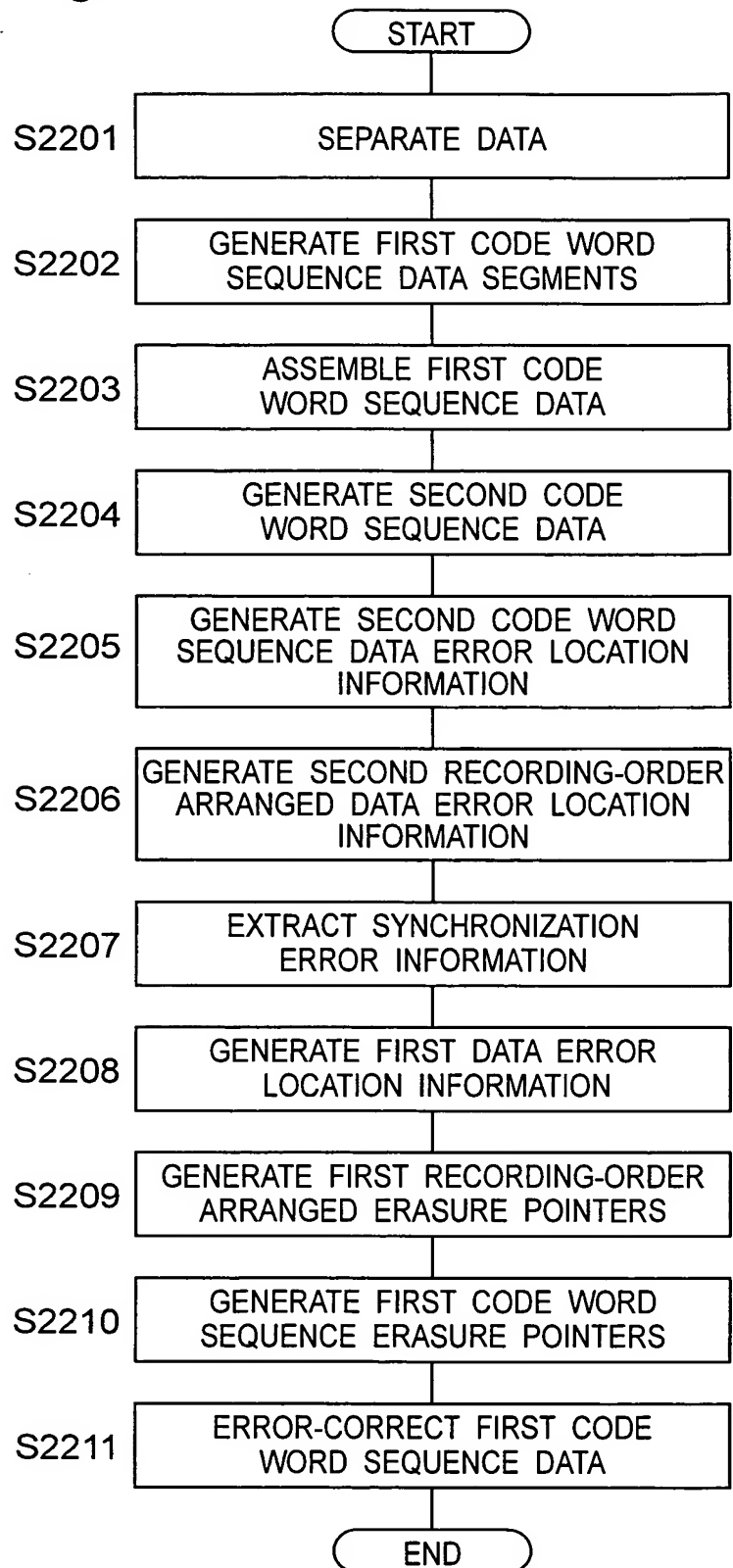


Fig.41

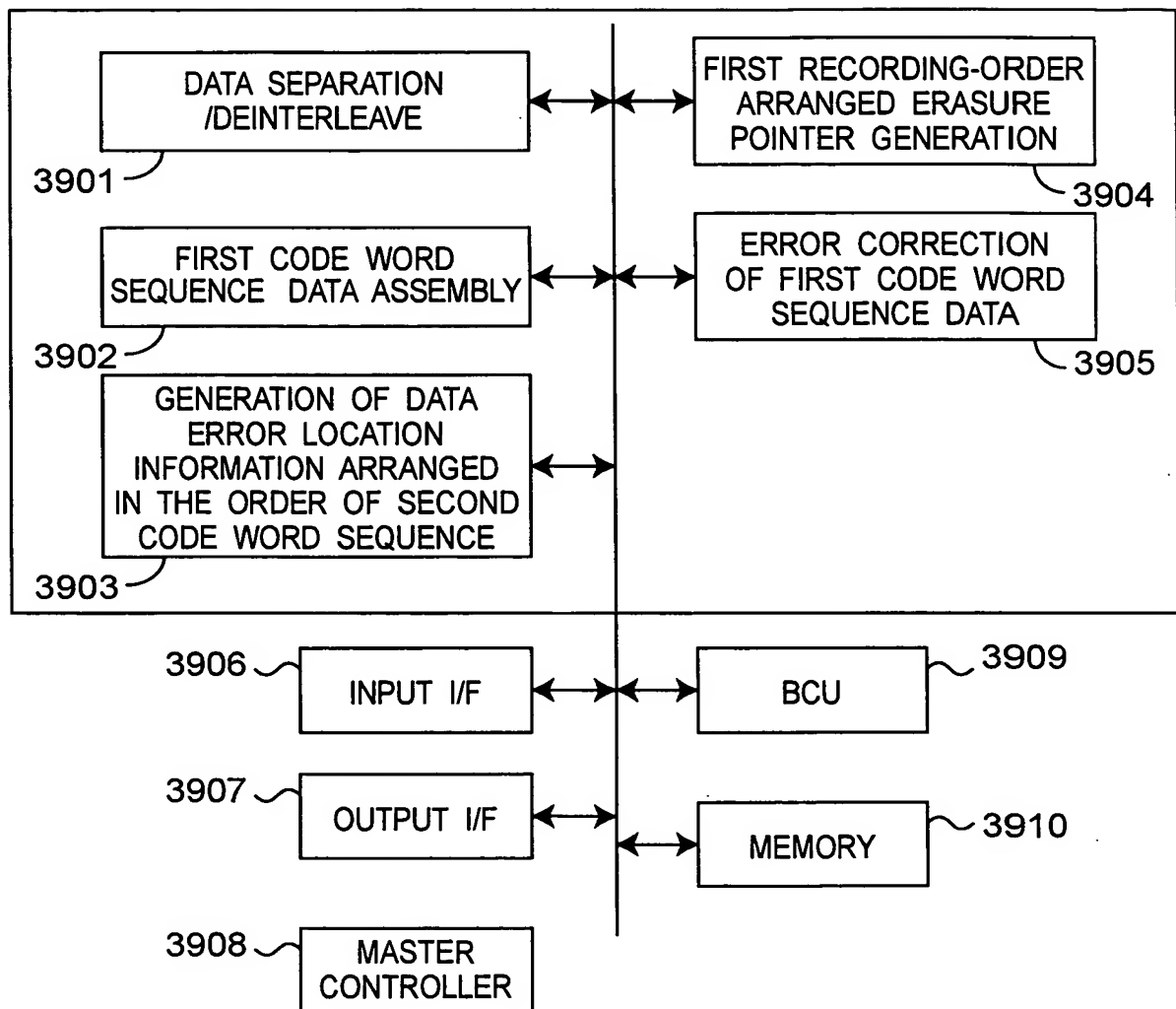


Fig.42

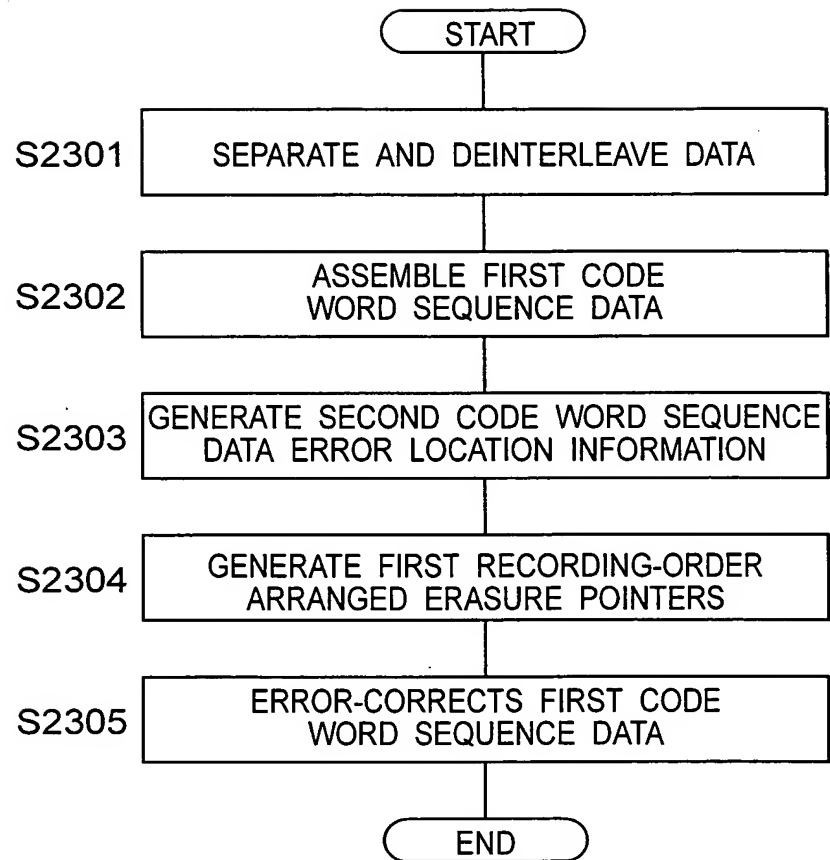


Fig. 43

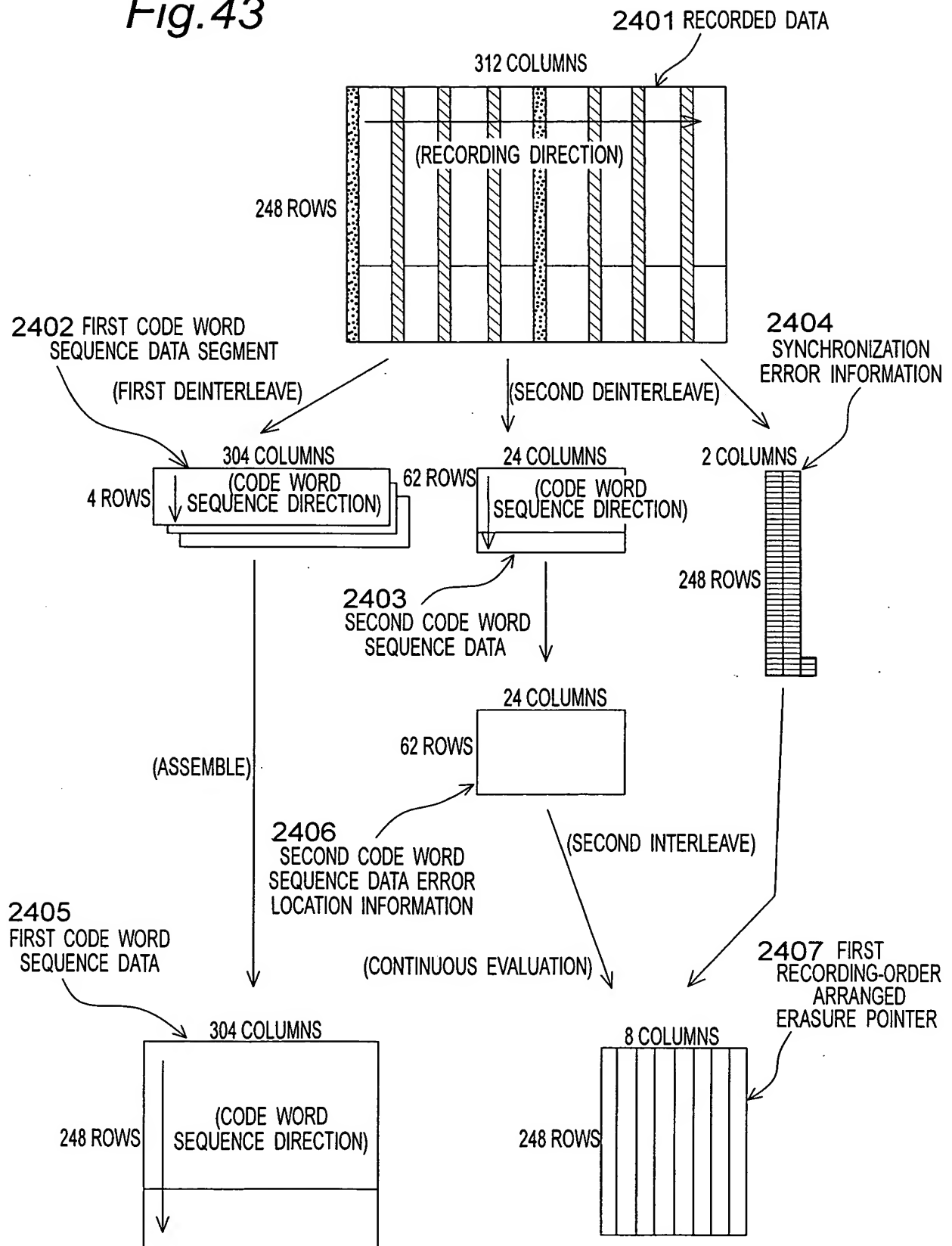


Fig.44

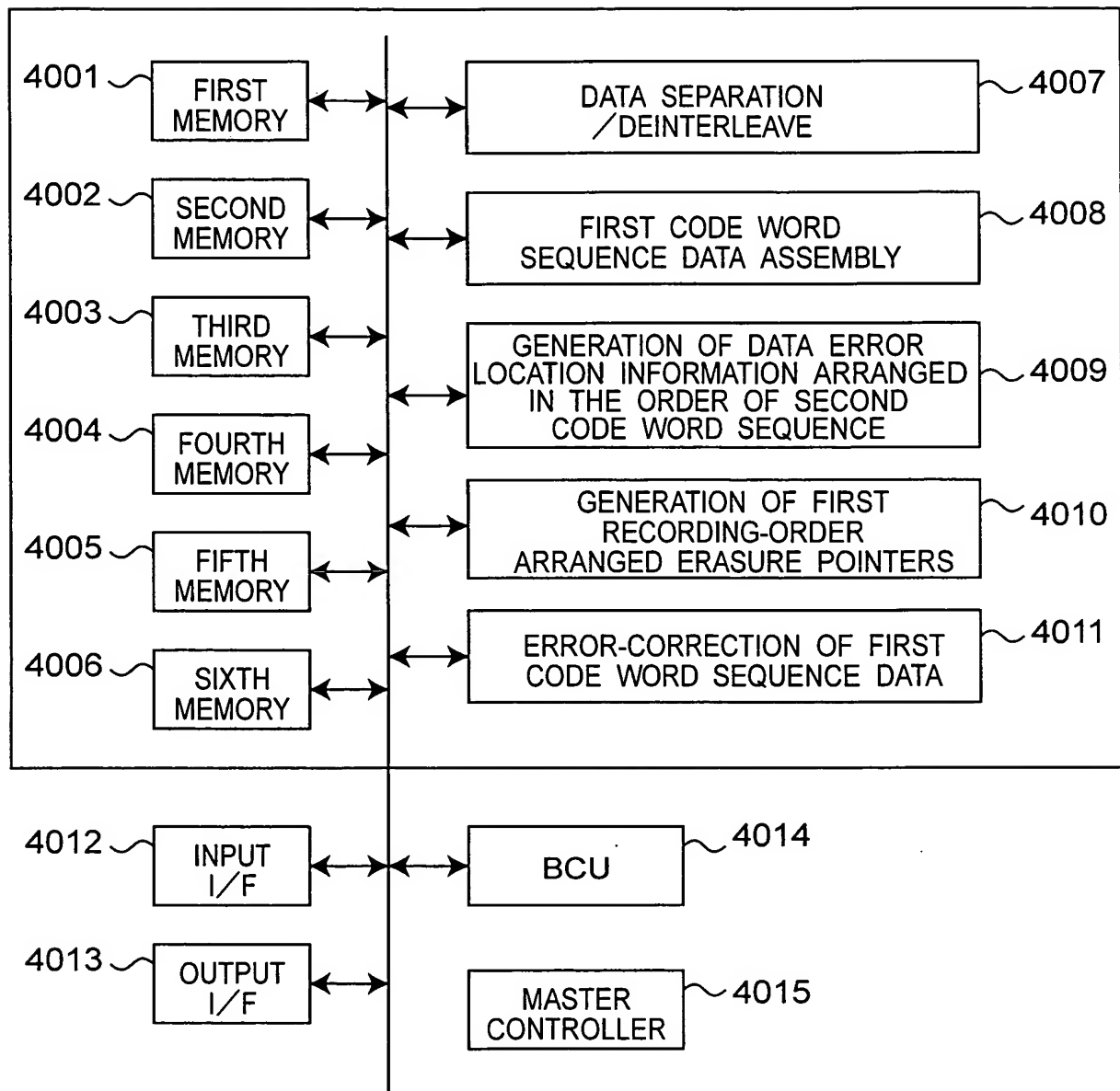


Fig.45

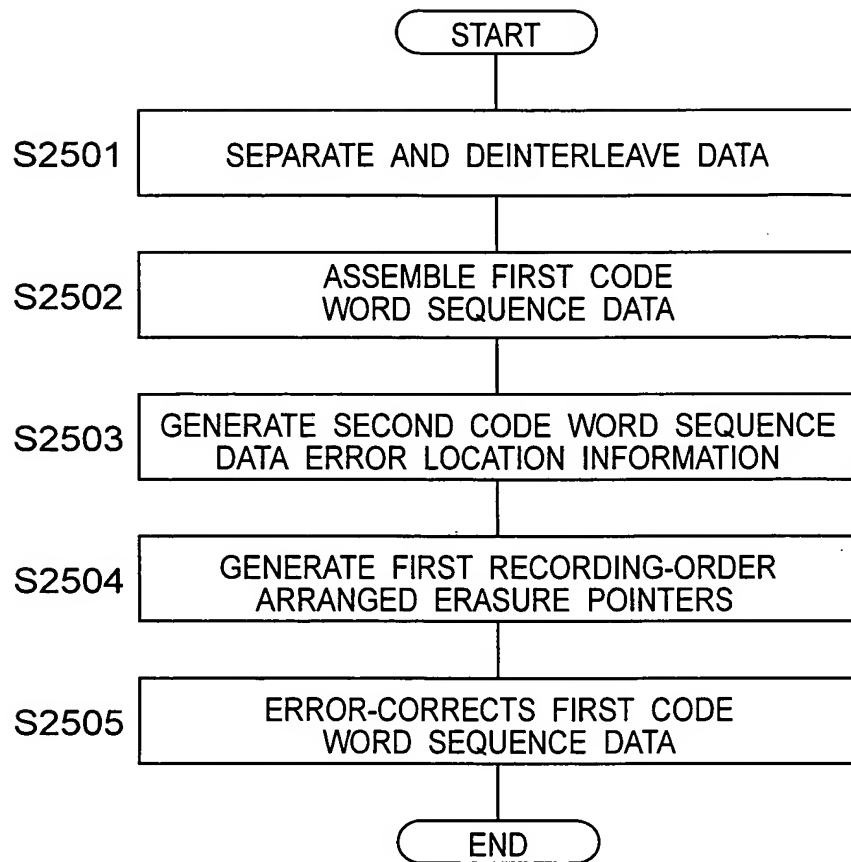


Fig.46

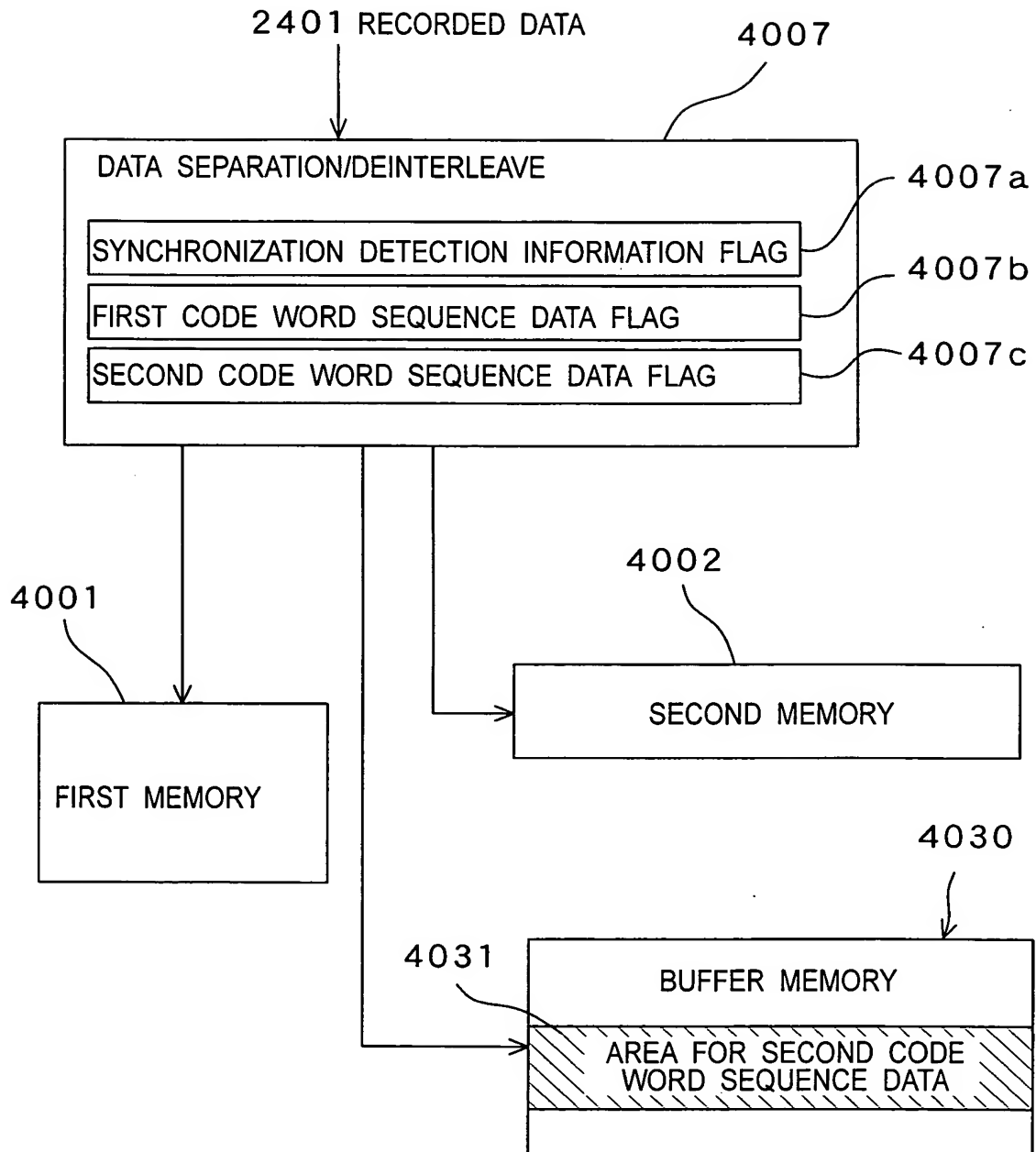


Fig.47

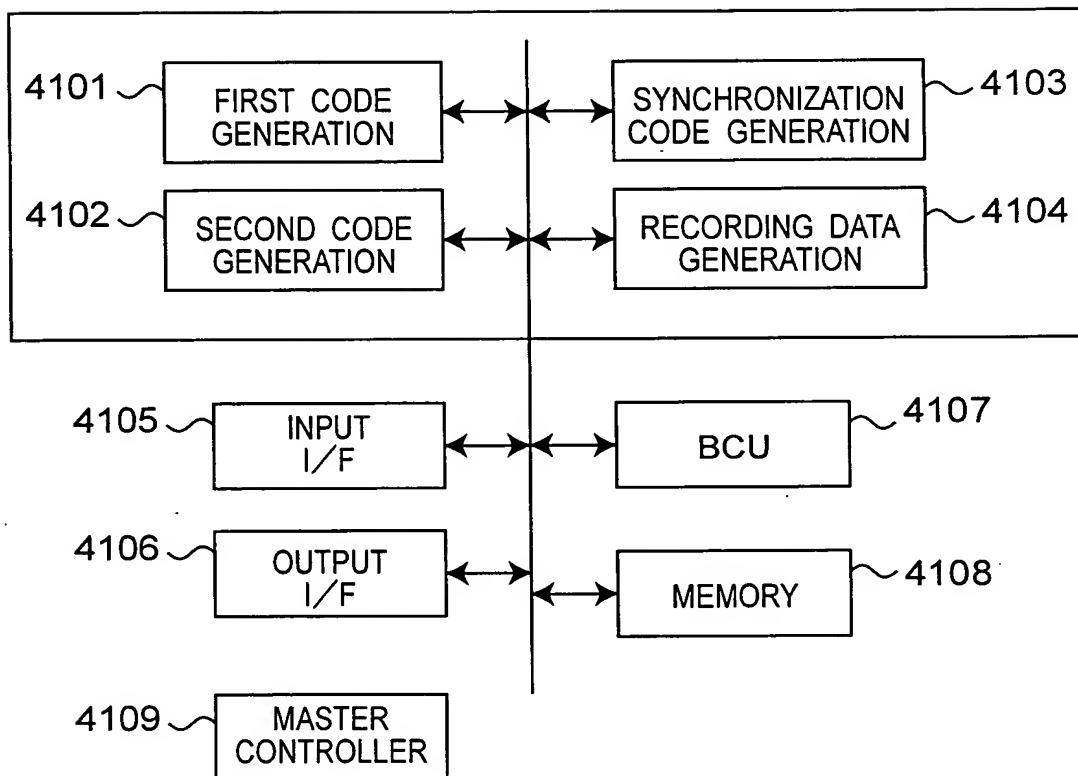


Fig.48

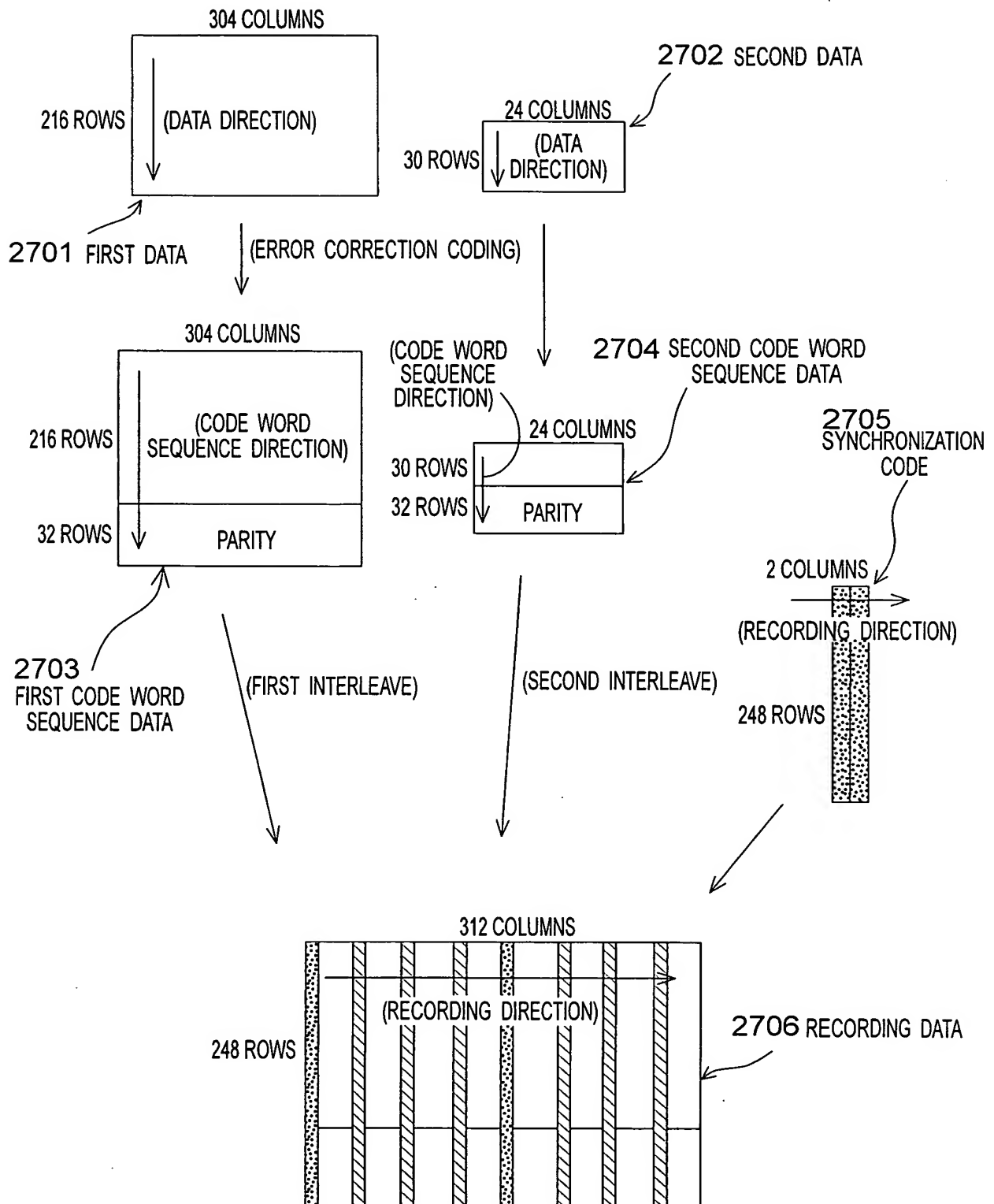


Fig.49

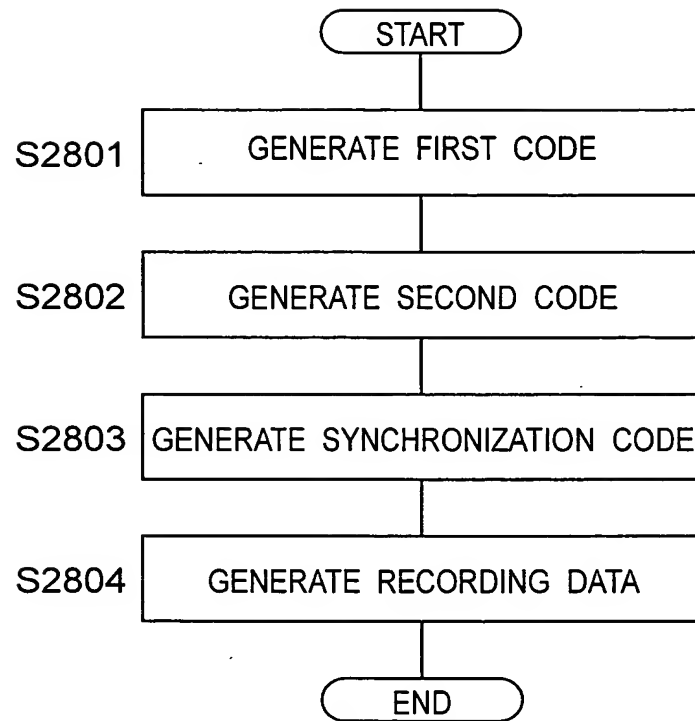


Fig.50

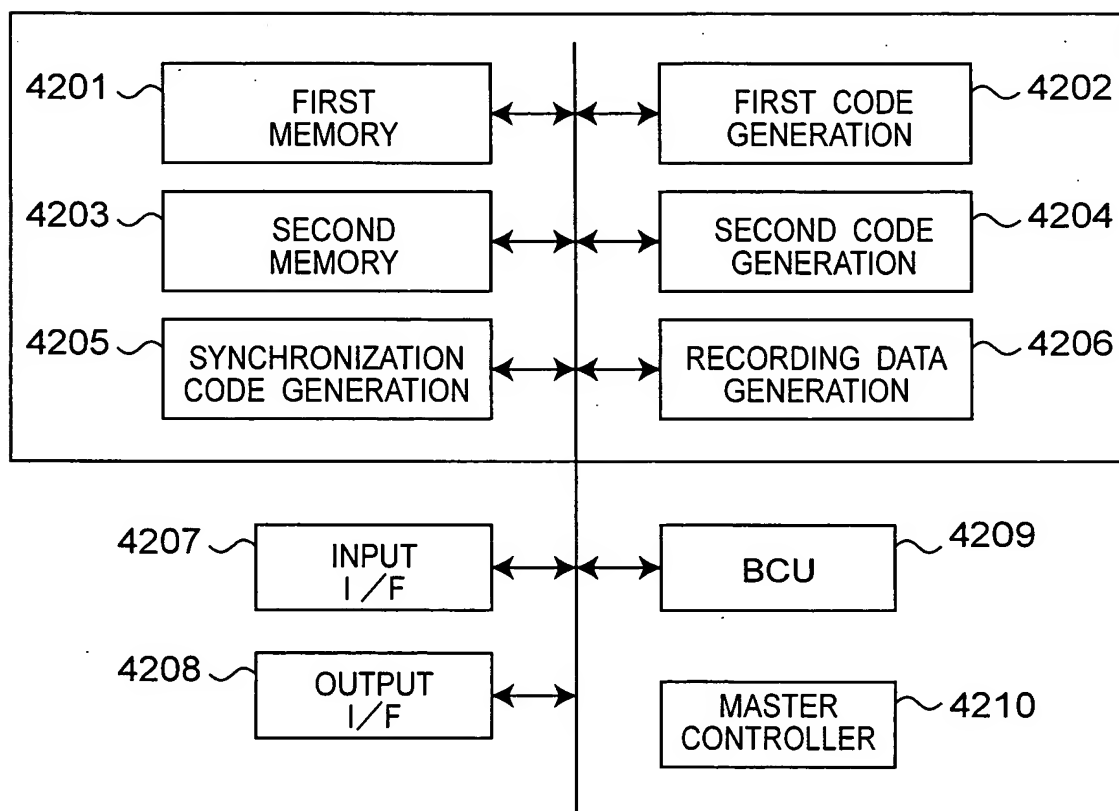


Fig.51

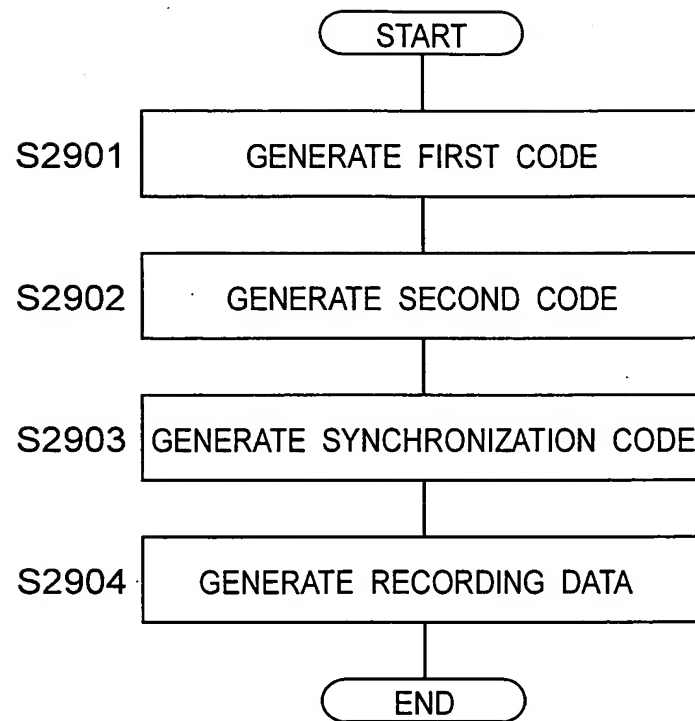


Fig.52

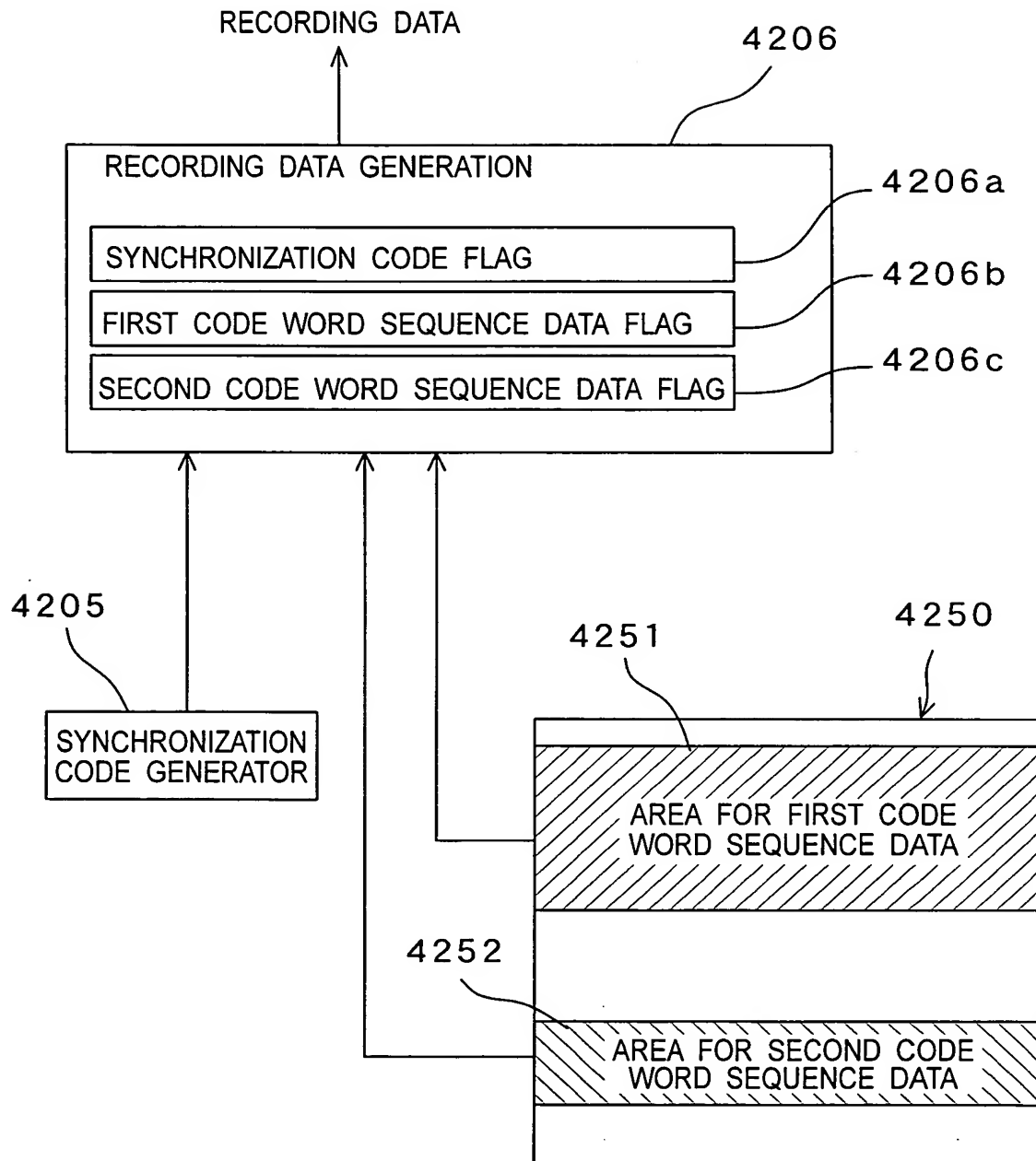


Fig.53

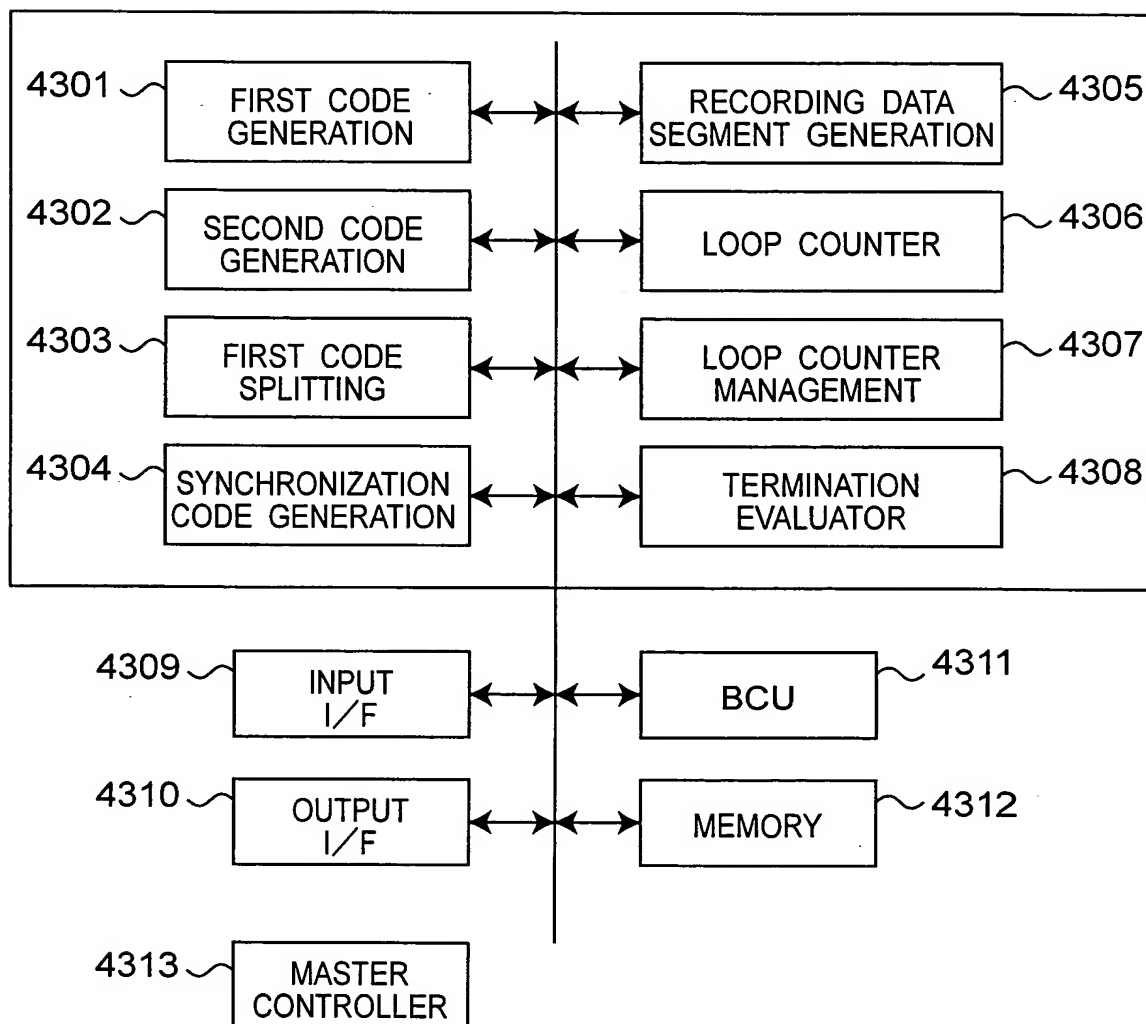


Fig. 54

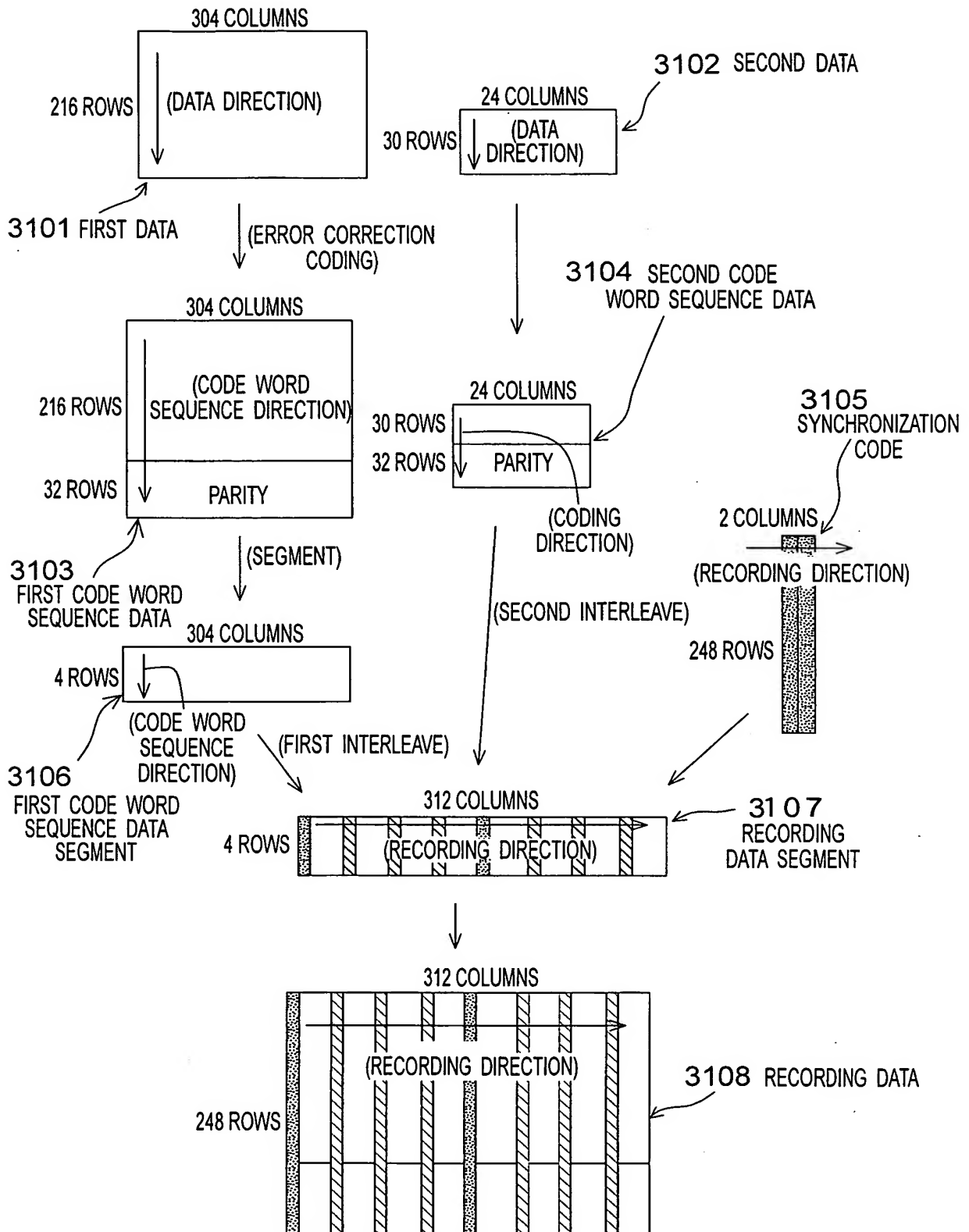


Fig.55

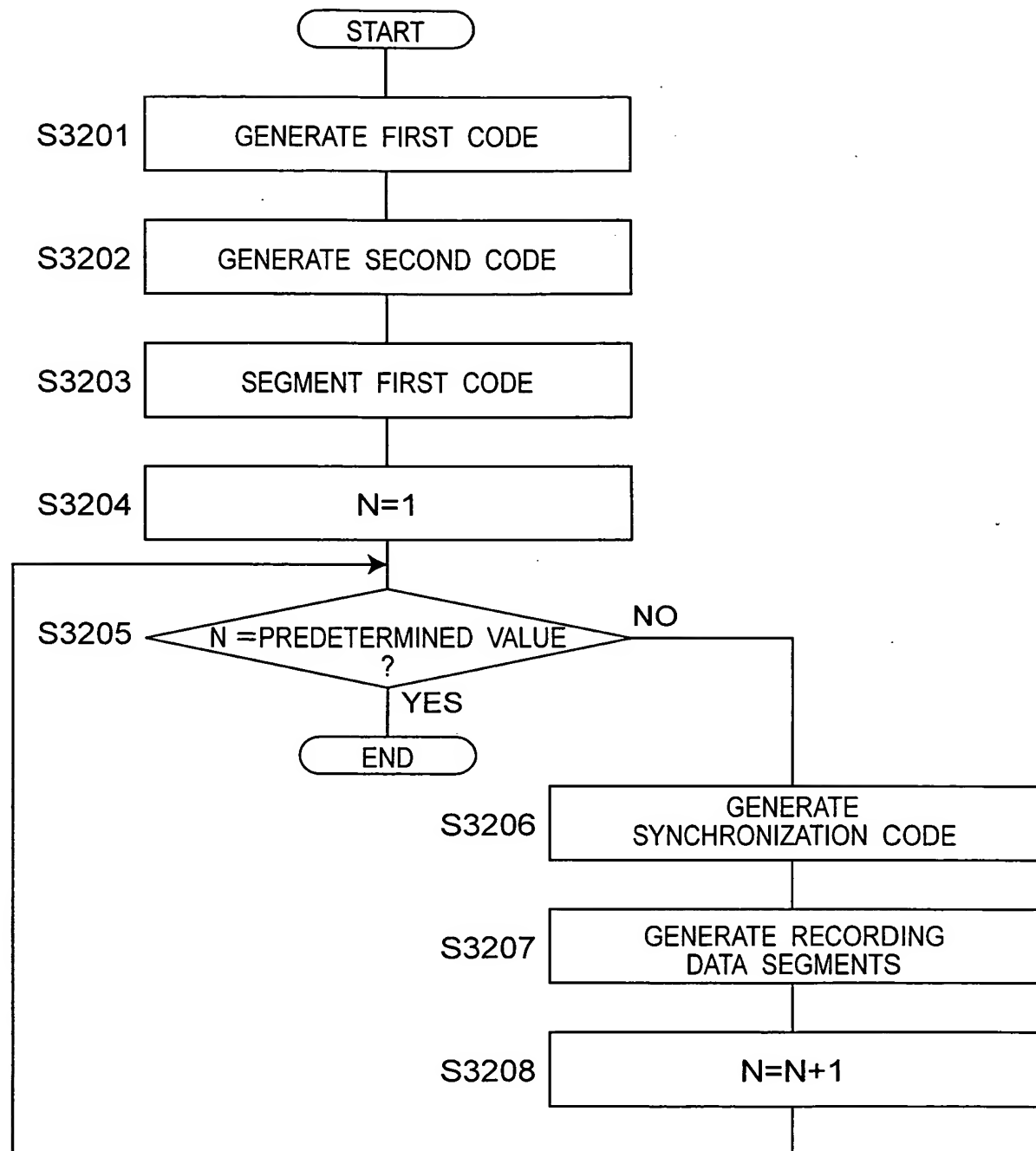


Fig.56

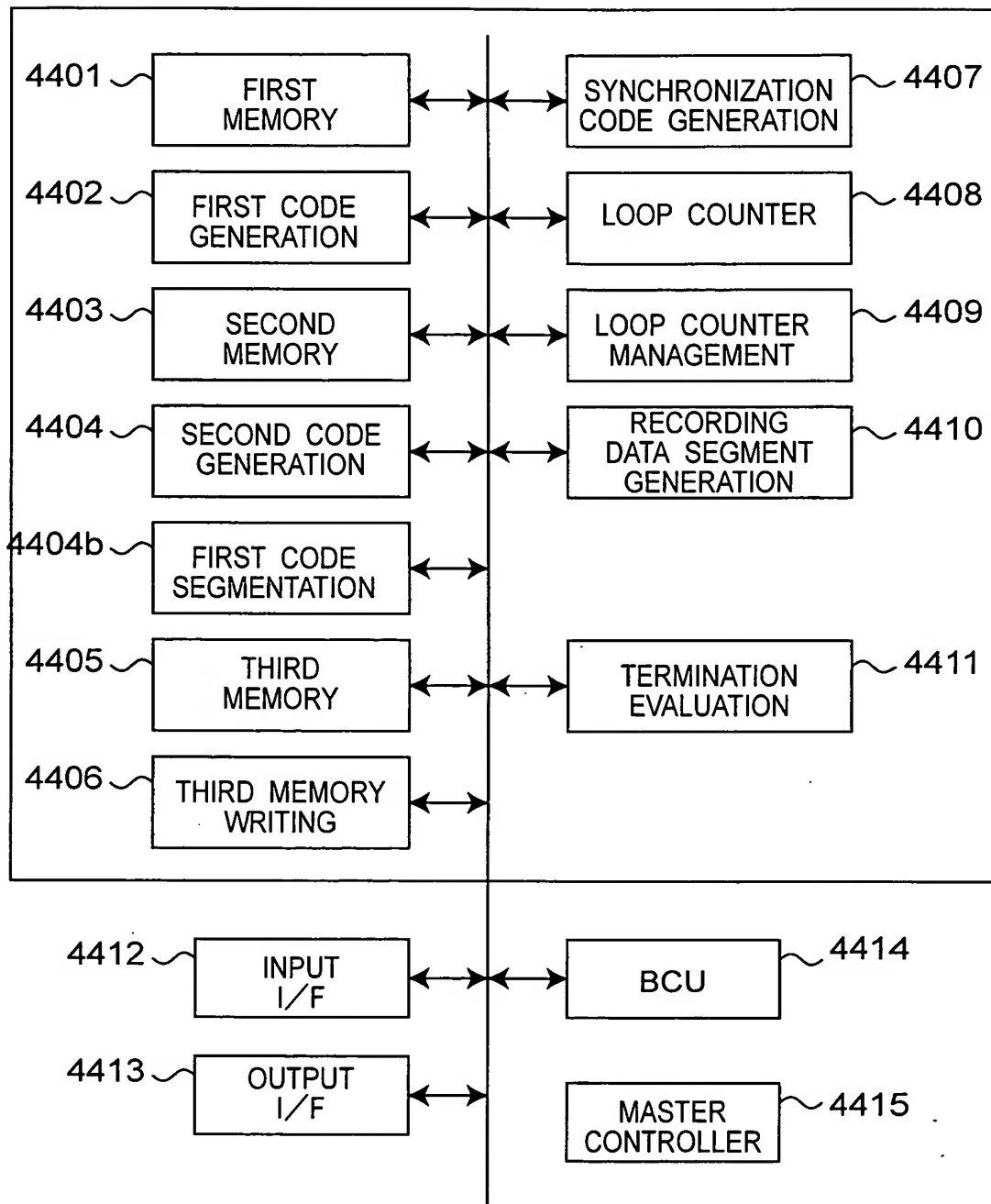


Fig.57

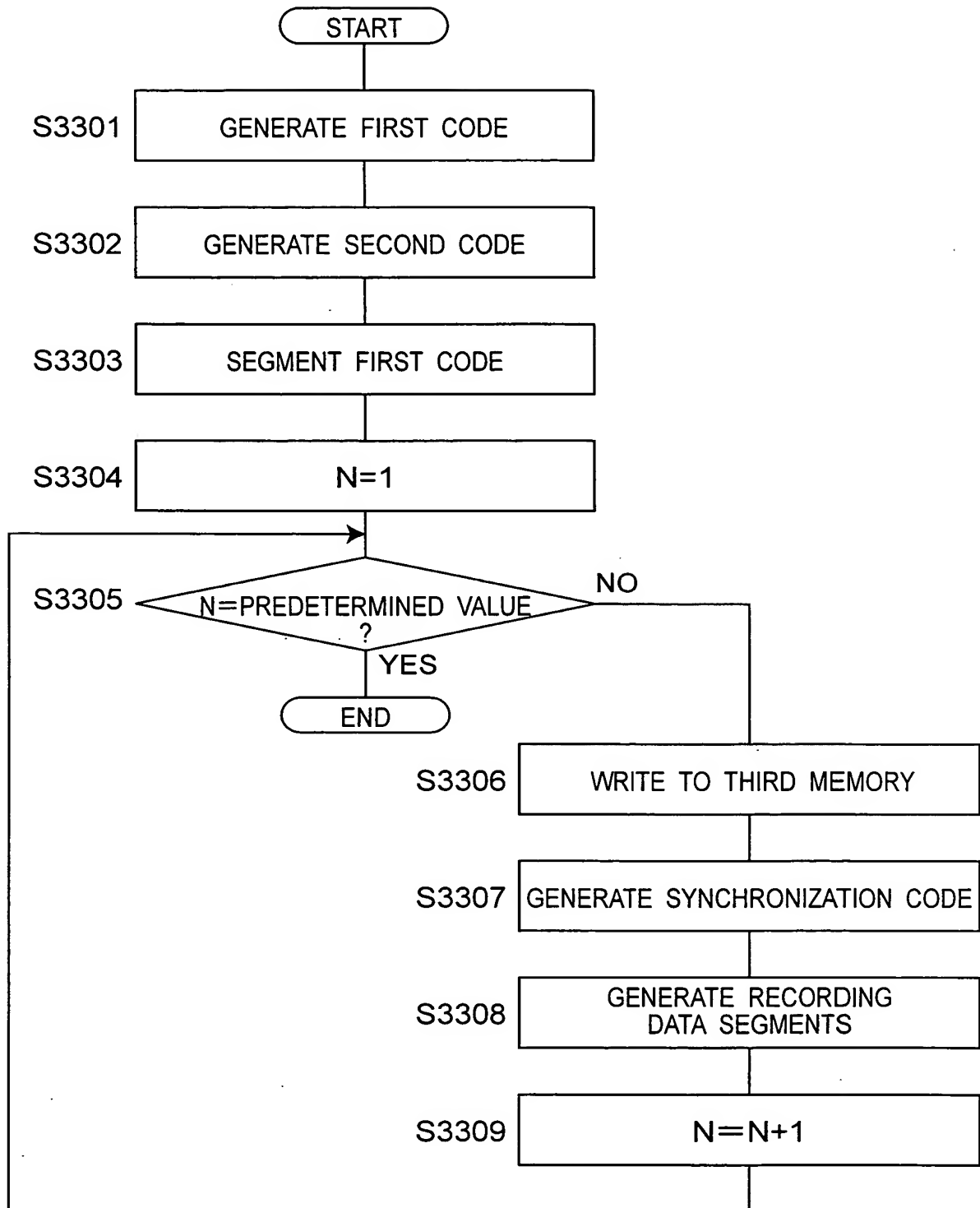


Fig. 58

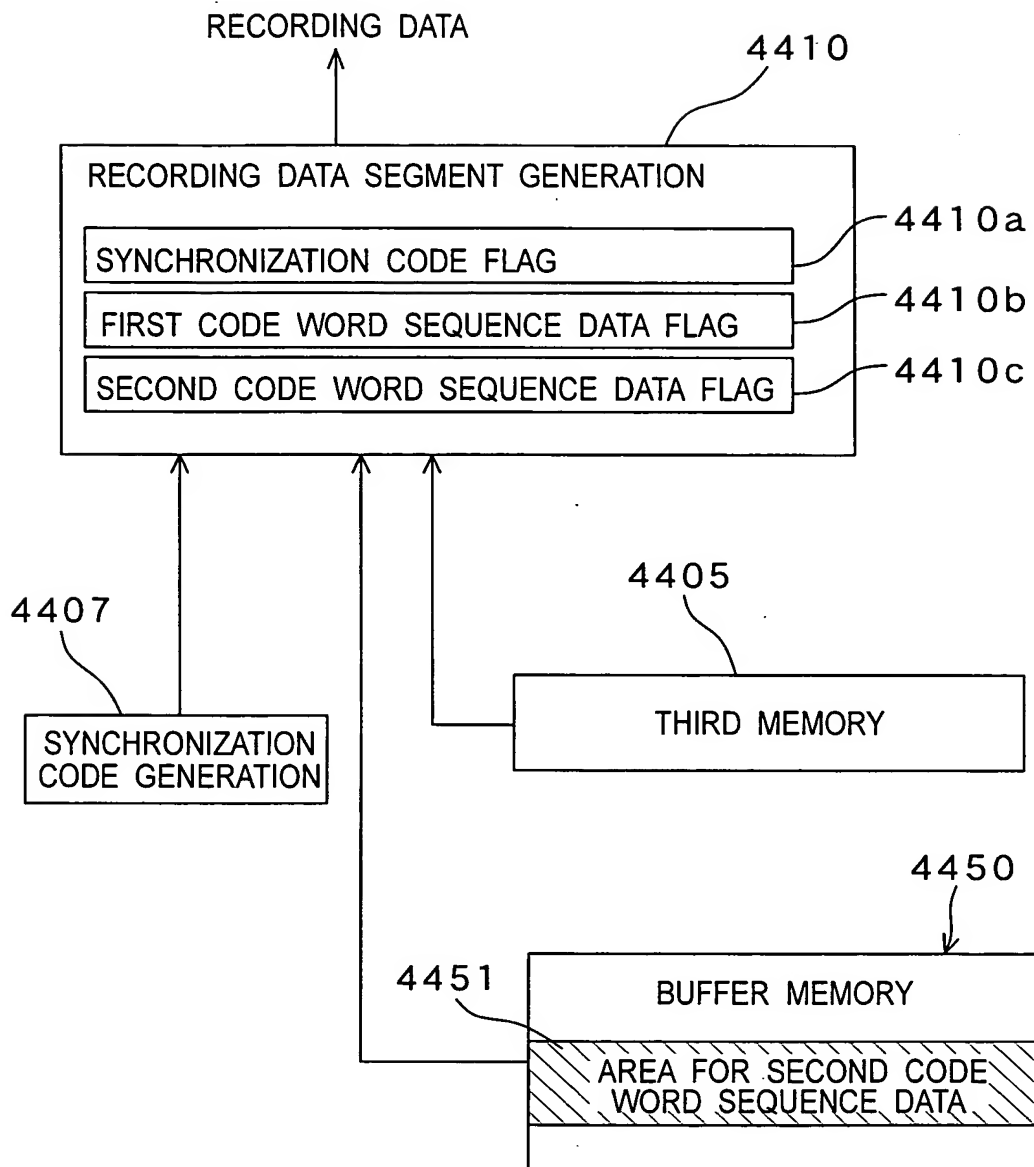


Fig. 59

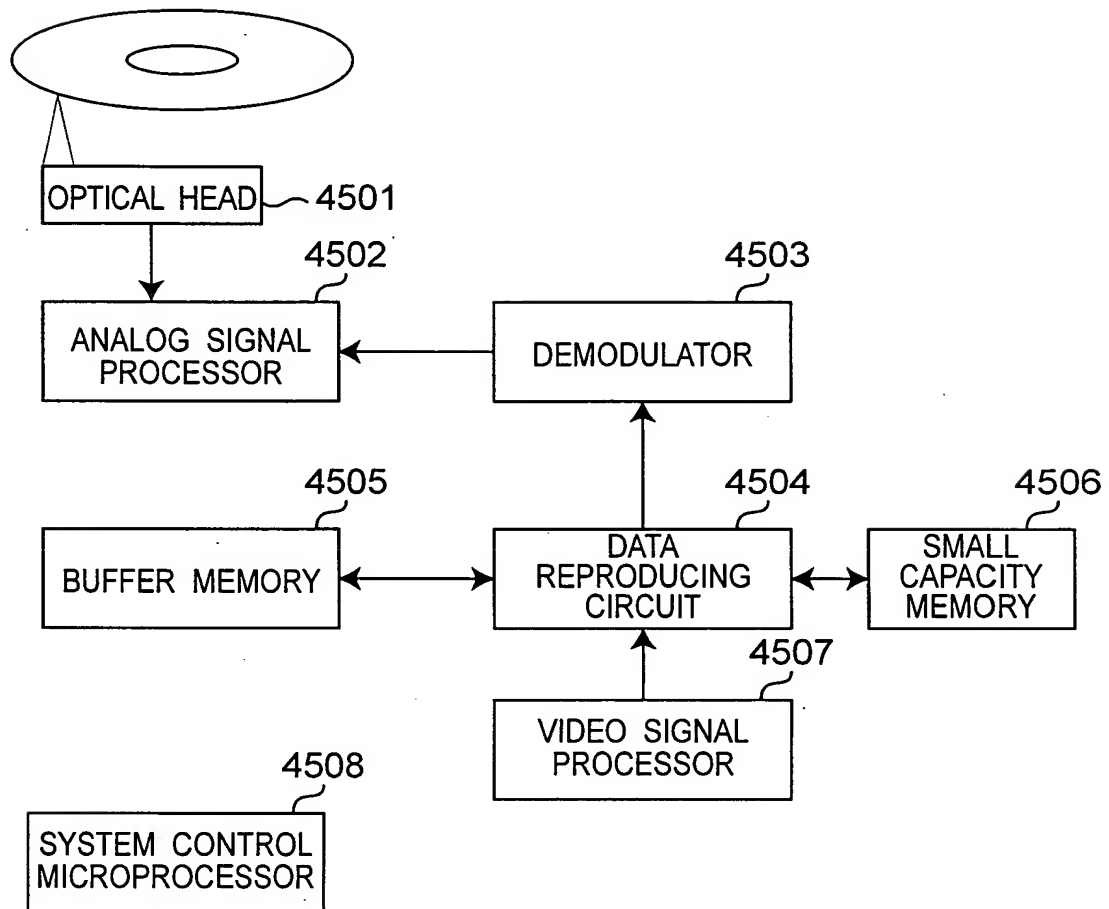


Fig. 60

